

SHEET TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	POWER MAP
05	P4 LGA775 A
06	P4 LGA775 B
07	P4 LGA775 C
08	P4 LGA775 D
09	GMCH-HOST
10	GMCH-DDRIII
11	GMCH-PCI E, DMI
12	GMCH-INT VGA
13	GMCH-GND
14	GMCH-PWR
15	DDRIII CHANNEL A 1,2
16	DDRIII CHANNEL B 1,2
17	DDRIII TERMINATION
18	PCI EXPRESS*16 SLOT
19	ICH9 PCI, USB, DMI, LAN
20	ICH9 GPIO, CTRL
21	ICH9 SATA, FAN PWM
22	ICH9 VCC, GND
23	CLOCK GEN CK505
24	PCI EXPRESS*1 ,PCI SLOT 1,2
25	ITE8712GB,RESET DRIVE
26	COM,LPT
27	BIOS,CI,HWM,KB/MS

SHEET TITLE

28	AZALIA ALC889A
29	AUDIO JACK
30	VCORE PWM ISL6327
31	DISCRETE POWER FAN CONTROL
32	ATX POWER
33	IDE/SATA JMB 363/368
34	LAN RTL8111C
35	FRONT PANEL,FUSB,FDD
36	
37	

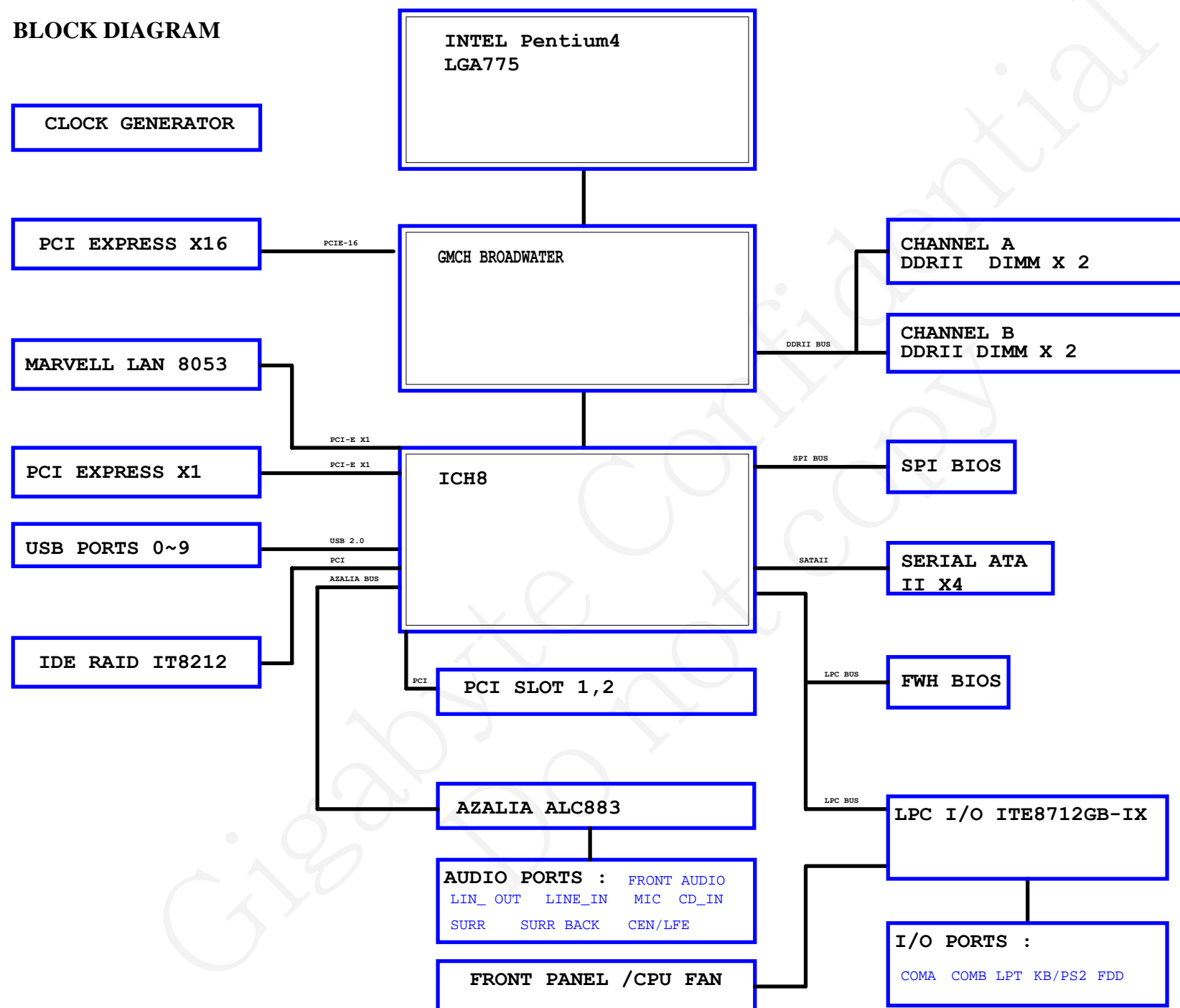
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Cover Sheet		
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Circuit or PCB layout change for next version

Component value change history

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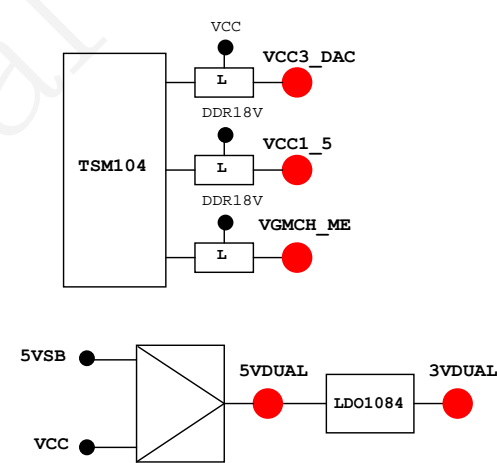
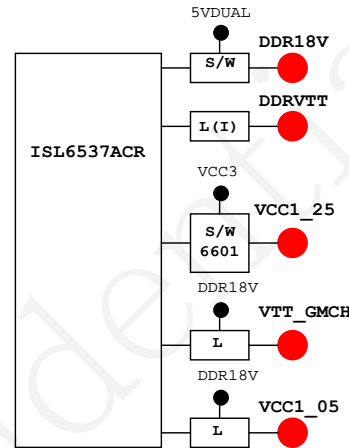
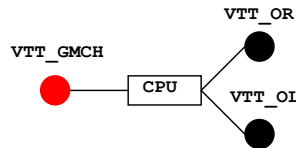
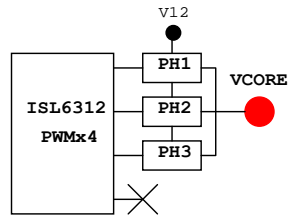
BLOCK DIAGRAM

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ICH8 GPIO LIST TABLE

PIN NAME	PWR WELL	AFTER/ PLTRST	USAGE	NOTE
GP0	MAIN	IN	-ACZ_DET	P/U 8.2K VCC3
GP1/TACH1	MAIN	IN	ICH_FAN_TACH1	P/U 8.2K VCC3
GP2/PIRQE#	MAIN	IN	-PIRQE	P/U 8.2K VCC3
GP3/PIRQF#	MAIN	IN	-PIRQF	P/U 8.2K VCC3
GP4/PIRQG#	MAIN	IN	-PIRQG	P/U 8.2K VCC3
GP5/PIRQH#	MAIN	IN	-PIRQH	P/U 8.2K VCC3
GP6/TACH2	MAIN	IN	ICH_FAN_TACH2	P/U 8.2K VCC3
GP7/TACH3	MAIN	IN	ICH_FAN_TACH3	P/U 8.2K VCC3
GP8	STBY	IN	GPIO8 (DUALBIOS_INPUT)	P/U 8.2K 3VDUAL
GP9	STBY	OUT	WOL_ONLY	P/D 100K GND
GP10	STBY	IN	CLGPIO1	P/U 8.2K 3VDUAL
GP11/SMBALERT#	STBY	OUT	-SMBALRT	P/U 8.2K 3VDUAL
GP12	STBY	IN	MB_ID0	P/U 8.2K 3VDUAL
GP13	STBY	IN	-LPCPME	P/U 8.2K 3VDUAL
GP14	STBY	IN	CLGPIO2	P/U 8.2K 3VDUAL
GP15	STBY	OUT	LAN_DISABLE (STP_PCI-)	N/A
GP16	MAIN	OUT/LOW	RESET	N/A
GP17/TACH0	MAIN	IN	ICH_FAN_TACH0	P/U 8.2K VCC3
GP18	MAIN	OUT	MB_ID1	P/U 8.2K VCC3
GP19	MAIN	IN	SATA1GP	P/U 8.2K VCC3
GP20	MAIN	OUT	-SPI_WP0	P/U 1K 3VCL
GP21	MAIN	IN	SATA0GP	P/U 8.2K VCC3
GP22	MAIN	IN	SCLOCK	P/U 8.2K VCC3
GP23	MAIN	OUT	-LDRQ1	P/U 8.2K VCC3
GP24	STBY	OUT	CLGPIO0	P/U 8.2K 3VDUAL
GP25	STBY	IN	MB_ID2 (STP_CPU-)	P/U 8.2K 3VDUAL
GP26/S4_STATE#	STBY	OUT	S4_STATE#	P/U 8.2K 3VDUAL
GP27	STBY	OUT/LOW	GPIO27 (EL_STATE0)	P/U 8.2K 3VDUAL
GP28	STBY	OUT/LOW	PWR_LED (EL_STATE1)	N/A
GP29/OC5#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP30/OC6#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP31/OC7#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP32	MAIN	OUT	DUAL_BIOS	P/U 100K+1M VCC3
GP33	MAIN	OUT	DUAL_BIOS	P/U 8.2K VCC3
GP34	MAIN	OUT/LOW	GPIO34/SMB_RST	N/A
GP35	MAIN	OUT	SATACLKREQ#	N/A
GP36	MAIN	IN	SATA2GP	P/U 8.2K VCC3
GP37	MAIN	IN	SATA3GP	P/U 8.2K VCC3
GP38	MAIN	IN	SLOAD	P/U 8.2K VCC3
GP39	MAIN	IN	GPIO39	P/D 8.2K GND
GP48	MAIN	IN	GPIO48	P/U 8.2K VCC3
GP49	MAIN	IN	CPUPWROK	P/U 100 VTT_OL

VCORE:3 PHASE PWM--ISL6312



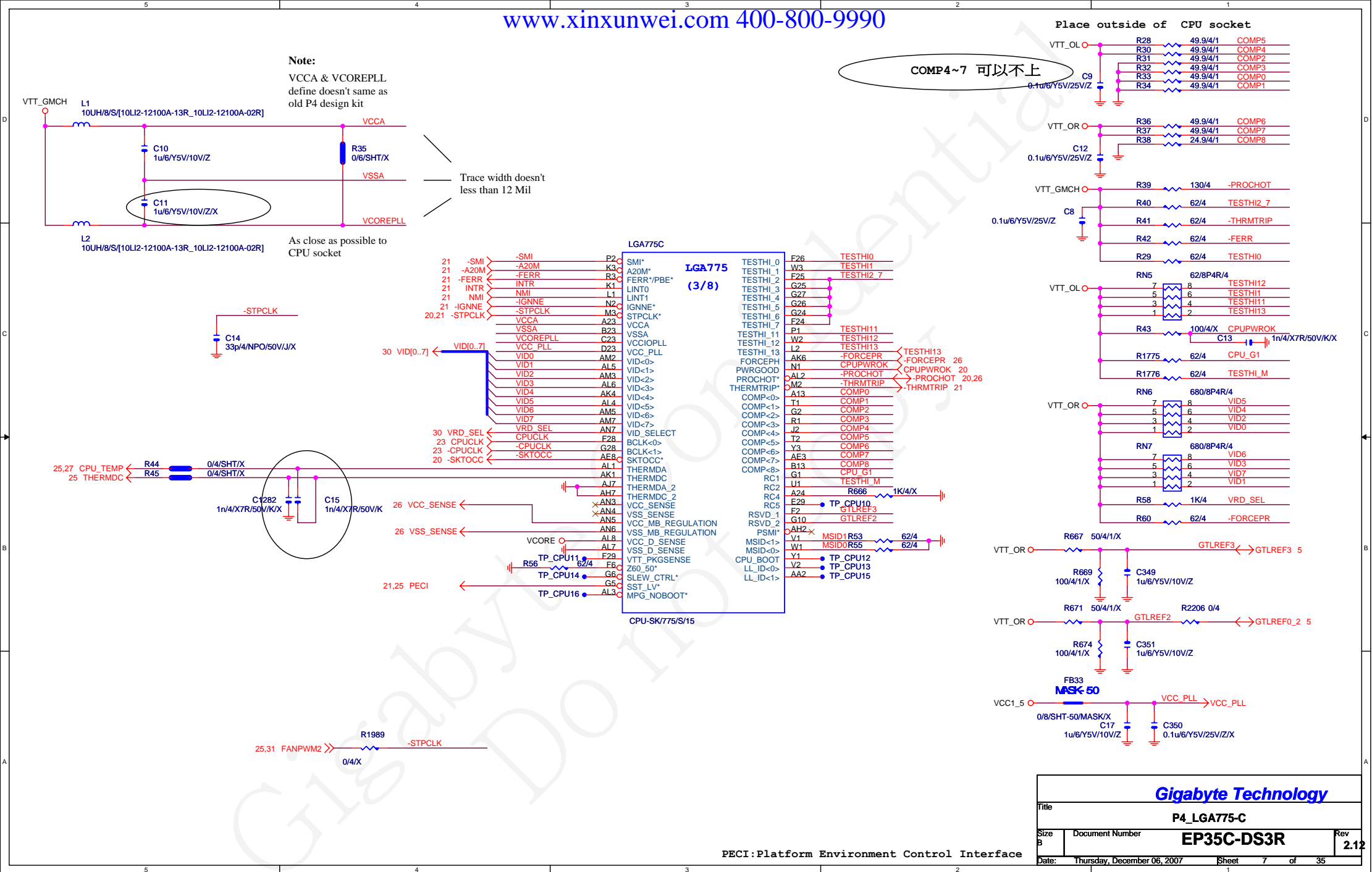
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CPU-SK/775/S/15

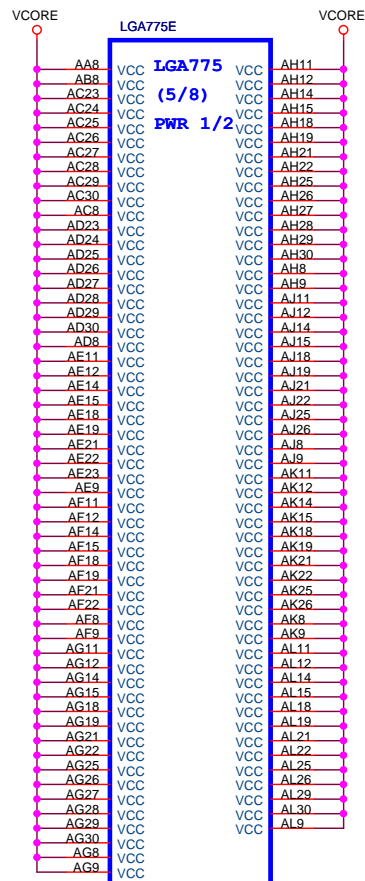
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COMP4~7 可以不上

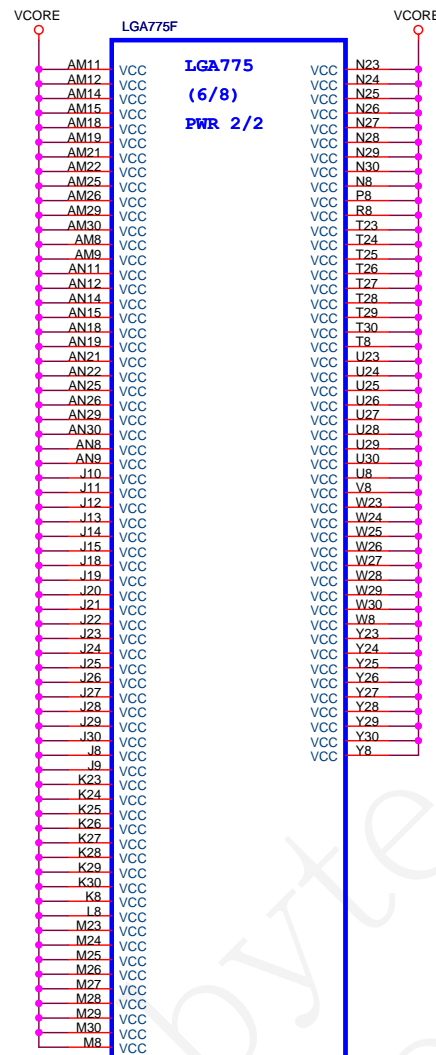


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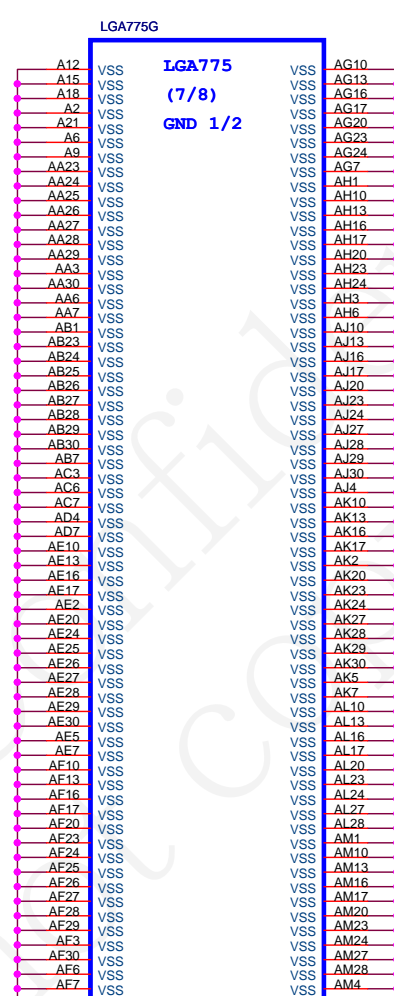
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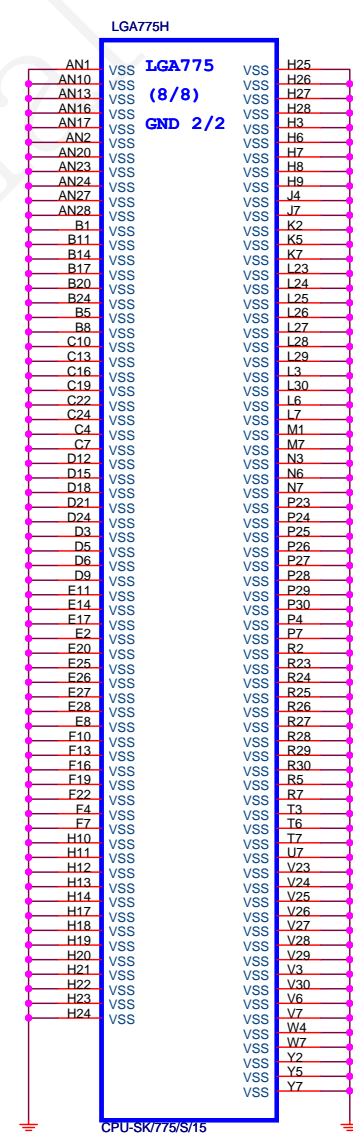
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CPU-SK/775/S/15



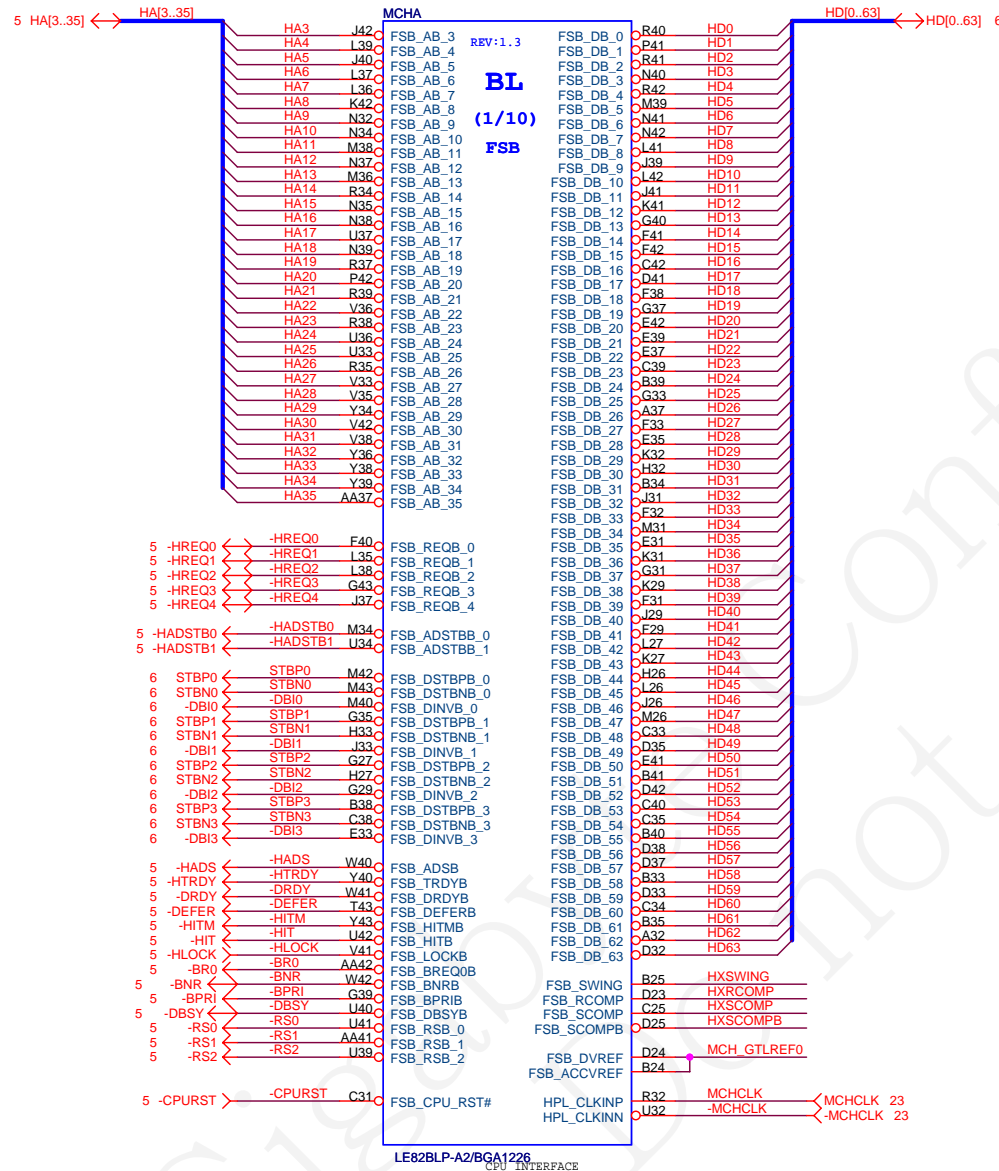
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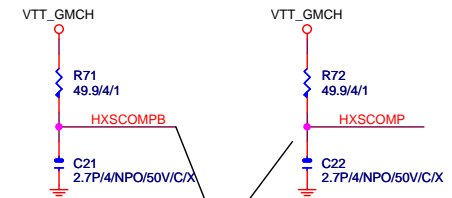
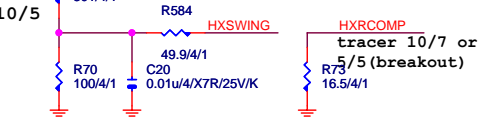
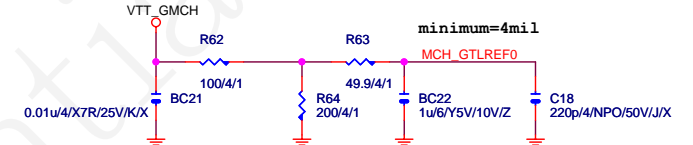
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tracer min 10/10 or (10/5
breakout), L1<3"



tracer 4/14 or
4/6 (breakout)



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GMCH-HOST

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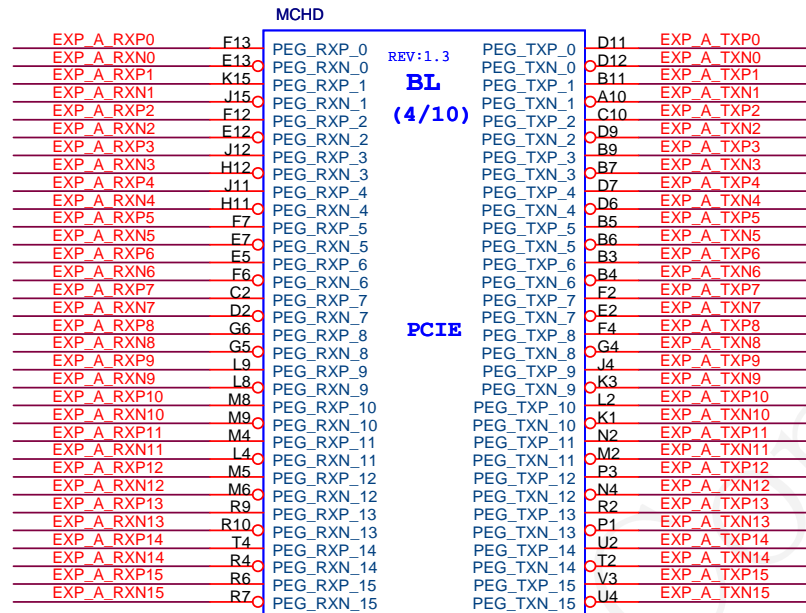
MCHB

REV:1.3

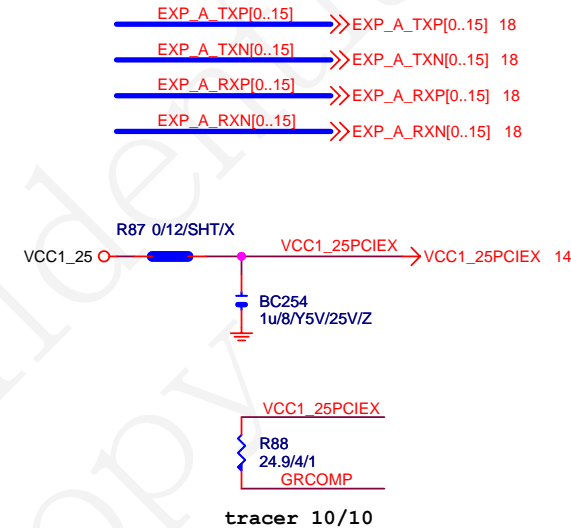
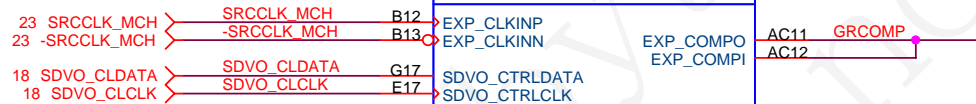
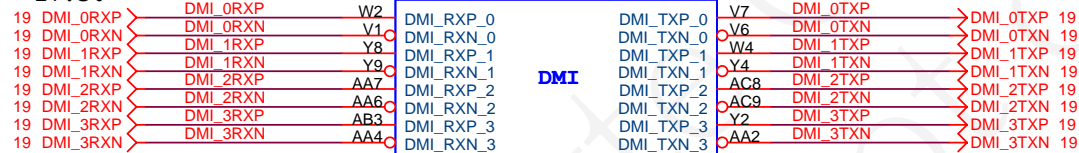
BL
(2/10)
DDR_A

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MAA11	AY25	DDR_A_MA.1	DDR_A_DQS.1
MAA12	BA23	DDR_A_MA.2	DDR_A_DM.0
MAA13	BB24	DDR_A_MA.3	
MAA14	AY23	DDR_A_MA.4	DDR_A_DQ.0
MAA15	BB22	DDR_A_MA.5	DDR_A_DQ.1
MAA16	BA22	DDR_A_MA.6	DDR_A_DQ.2
MAA17	BB21	DDR_A_MA.7	DDR_A_DQ.3
MAA18	AW21	DDR_A_MA.8	DDR_A_DQ.4
MAA19	BA21	DDR_A_MA.9	DDR_A_DQ.5
MAA20	BB21	DDR_A_MA.10	DDR_A_DQ.6
MAA21	BC20	DDR_A_MA.11	DDR_A_DQ.7
MAA22	AY20	DDR_A_MA.12	
MAA23	BB20	DDR_A_MA.13	DDR_A_DQS.1
MAA24	BA19	DDR_A_MA.14	DDR_A_DQS.1
MAA25	BB19	DDR_A_MA.15	DDR_A_DM.1
MAA26	AY19	DDR_A_MA.16	
MAA27	BB19	DDR_A_MA.17	DDR_A_DM.2
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MAA29	BB18	DDR_A_MA.19	
MAA30	AY18	DDR_A_MA.20	
MAA31	BB18	DDR_A_MA.21	
MAA32	BA17	DDR_A_MA.22	
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MAA34	AY17	DDR_A_MA.24	
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MAA37	BB16	DDR_A_MA.27	
MAA38	AY16	DDR_A_MA.28	
MAA39	BB16	DDR_A_MA.29	
MAA40	BA15	DDR_A_MA.30	
MAA41	BB15	DDR_A_MA.31	
MAA42	AY15	DDR_A_MA.32	
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MAA50	AY13	DDR_A_MA.40	
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MAA52	BA12	DDR_A_MA.42	
MAA53	BB12	DDR_A_MA.43	
MAA54	AY12	DDR_A_MA.44	
MAA55	BB12	DDR_A_MA.45	
MAA56	BA11	DDR_A_MA.46	
MAA57	BB11	DDR_A_MA.47	
MAA58	AY11	DDR_A_MA.48	
MAA59	BB11	DDR_A_MA.49	
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MAA61	BB10	DDR_A_MA.51	
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MAA64	BA09	DDR_A_MA.54	
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MAA66	AY09	DDR_A_MA.56	
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MAA68	BA08	DDR_A_MA.58	
MAA69	BB08	DDR_A_MA.59	
MAA70	AY08	DDR_A_MA.60	
MAA71	BB08	DDR_A_MA.61	
MAA72	BA07	DDR_A_MA.62	
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MAA261	BB00	DDR_A_MA.251	
MAA262	AY00	DDR_A_MA.252	
MAA263	BB00	DDR_A_MA.253	
MAA264	BA00	DDR_A_MA.254	
MAA265	BB00	DDR_A_MA.255	
MAA266	AY00	DDR_A_MA.256	
MAA267	BB00	DDR_A_MA.257	
MAA268	BA00	DDR_A_MA.258	
MAA269	BB00	DDR_A_MA.259	
MAA270	AY00	DDR_A_MA.260	
MAA271	BB00	DDR_A_MA.261	
MAA272	BA00	DDR_A_MA.262	
MAA273	BB00	DDR_A_MA.263	
MAA274	AY00	DDR_A_MA.264	
MAA275	BB00	DDR_A_MA.265	
MAA276	BA00	DDR_A_MA.266	
MAA277	BB00	DDR_A_MA.267	
MAA278	AY00	DDR_A_MA.268	
MAA279	BB00	DDR_A_MA.269	
MAA280	BA00	DDR_A_MA.270	
MAA281	BB00	DDR_A_MA.271	
MAA282	AY00	DDR_A_MA.272	
MAA283	BB00	DDR_A_MA.273	
MAA284	BA00	DDR_A_MA.274	
MAA285	BB00	DDR_A_MA.275	
MAA286	AY00	DDR_A_MA.276	
MAA287	BB00	DDR_A_MA.277	
MAA288	BA00	DDR_A_MA.278	
MAA289	BB00	DDR_A_MA.279	
MAA290	AY00	DDR_A_MA.280	
MAA291	BB00	DDR_A_MA.281	
MAA292	BA00	DDR_A_MA.282	
MAA293	BB00	DDR_A_MA.283	
MAA294	AY00	DDR_A_MA.284	
MAA295	BB00	DDR_A_MA.285	
MAA296	BA00	DDR_A_MA.286	
MAA297	BB00	DDR_A_MA.287	
MAA298	AY00	DDR_A_MA.288	
MAA299	BB00	DDR_A_MA.289	
MAA300	BA00	DDR_A_MA.290	
MAA301	BB00	DDR_A_MA.291	
MAA302	AY00	DDR_A_MA.292	
MAA303	BB00	DDR_A_MA.293	
MAA304	BA00	DDR_A_MA.294	
MAA305	BB00	DDR_A_MA.295	
MAA306	AY00	DDR_A_MA.296	
MAA307	BB00	DDR_A_MA.297	
MAA308	BA00	DDR_A_MA.298	
MAA309	BB00	DDR_A_MA.299	
MAA310	AY00	DDR_A_MA.300	
MAA311	BB00	DDR_A_MA.301	
MAA312	BA00	DDR_A_MA.302	
MAA313	BB00	DDR_A_MA.303	
MAA314	AY00	DDR_A_MA.304	
MAA315	BB00	DDR_A_MA.305	
MAA316	BA00	DDR_A_MA.306	
MAA317	BB0		

PCIEX16:15/4/8/4/15
Impedance=95 +- 17.5%

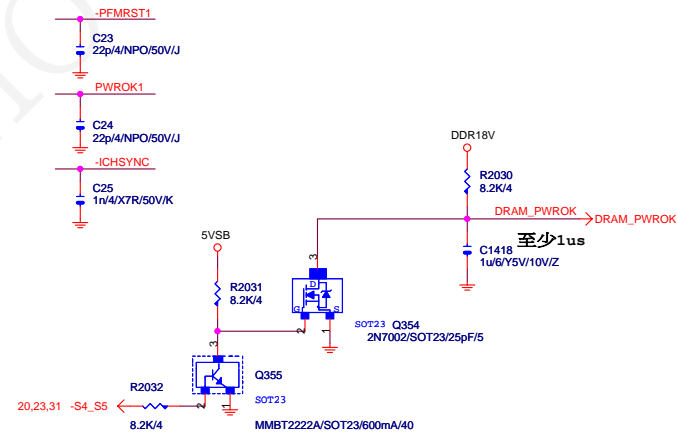
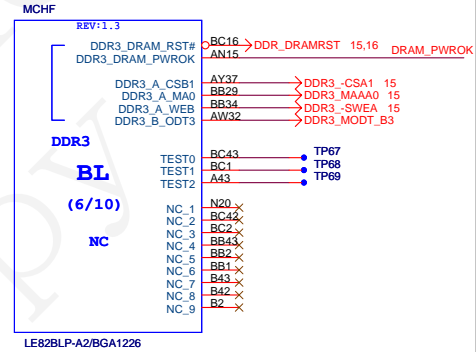


DMI:12/4/8/4/12
Impedance=95 +- 17.5%



Gigabyte Technology

Title		
GMCH-PCI E & DMI		
Size Custom	Document Number EP35C-DS3R	Rev 2.12
Date:	Thursday, December 06, 2007	Sheet 11 of 35



MCHG

BC37	VSS_1	REV:1.3	VSS_181	AE24
BC32	VSS_2		VSS_182	AE22
BC28	VSS_3		VSS_183	AE20
BC24	VSS_4		VSS_184	AE4
BC10	VSS_5		VSS_185	AE3
BC5	VSS_6		VSS_186	AE2
BB7	VSS_7		VSS_187	AD42
AY41	VSS_8		VSS_188	AD39
AY4	VSS_9		VSS_189	AD37
AW43	VSS_10		VSS_190	AD35
AW41	VSS_11		VSS_191	AD33
AV37	VSS_12		VSS_192	AD25
AV35	VSS_13		VSS_193	AD23
AV27	VSS_14		VSS_194	AD21
AV23	VSS_15		VSS_195	AD19
AV21	VSS_16		VSS_196	AC38
AV17	VSS_17		VSS_197	AC35
AV11	VSS_18		VSS_198	AC24
AV9	VSS_19		VSS_199	AC22
AV7	VSS_20		VSS_200	AC20
AV2	VSS_21		VSS_201	AC10
AU42	VSS_22		VSS_202	AC7
AU38	VSS_23		VSS_203	AC5
AU32	VSS_24		VSS_204	AB43
AU24	VSS_25		VSS_205	AB25
AU20	VSS_26		VSS_206	AB23
AU6	VSS_27		VSS_207	AB21
AU4	VSS_28		VSS_208	AB19
AT31	VSS_29		VSS_209	AB2
AT29	VSS_30		VSS_210	AB1
AT15	VSS_31		VSS_211	AA38
AT13	VSS_32		VSS_212	AA35
AT12	VSS_33		VSS_213	AA24
AR38	VSS_34		VSS_214	AA22
AR33	VSS_35		VSS_215	AA20
AR32	VSS_36		VSS_216	AA8
AR27	VSS_37		VSS_217	AA5
AR26	VSS_38		VSS_218	Y42
AR23	VSS_39		VSS_219	Y37
AR21	VSS_40		VSS_220	Y35
AR20	VSS_41		VSS_221	Y33
AR17	VSS_42		VSS_222	Y25
AR9	VSS_43		VSS_223	Y23
AR6	VSS_44		VSS_224	Y21
AP43	VSS_45		VSS_225	Y19
AP24	VSS_46		VSS_226	Y10
AP18	VSS_47		VSS_227	Y7
AP1	VSS_48		VSS_228	Y5
AN38	VSS_49		VSS_229	Y1
AN31	VSS_50		VSS_230	W24
AN29	VSS_51		VSS_231	W22
AN24	VSS_52		VSS_232	W20
AN23	VSS_53		VSS_233	W3
AN20	VSS_54		VSS_234	V43
AN13	VSS_55		VSS_235	V39
AN12	VSS_56		VSS_236	V37
AN11	VSS_57		VSS_237	V34
AN4	VSS_58		VSS_238	V32
AM42	VSS_59		VSS_239	V11
AM40	VSS_60		VSS_240	V8
AM36	VSS_61		VSS_241	V5
AM33	VSS_62		VSS_242	V2
AM29	VSS_63		VSS_243	U38
AM24	VSS_64		VSS_244	U35
AM23	VSS_65		VSS_245	U8
AM20	VSS_66		VSS_246	U7
AM11	VSS_67		VSS_247	U5
AM9	VSS_68		VSS_248	T42
AM7	VSS_69		VSS_249	T1
AM4	VSS_70		VSS_250	R36
AL36	VSS_71		VSS_251	R33
AL33	VSS_72		VSS_252	R31
AL31	VSS_73		VSS_253	R11
AK43	VSS_74		VSS_254	R8
AJ39	VSS_75		VSS_255	R5
AJ36	VSS_76		VSS_256	R3
AJ33	VSS_77		VSS_257	P43
AJ32	VSS_78		VSS_258	P30
AH42	VSS_79		VSS_259	P21
AG37	VSS_80		VSS_260	P18
AG34	VSS_81		VSS_261	P17
AF43	VSS_82		VSS_262	P2
AF37	VSS_83		VSS_263	N36
AF36	VSS_84		VSS_264	N33
AF10	VSS_85		VSS_265	N31
AF9	VSS_86		VSS_266	N27
AF8	VSS_87		VSS_267	N21
AF7	VSS_88		VSS_268	N13
AF6	VSS_89		VSS_269	N10
AF5	VSS_90		VSS_270	N7

LE82BLP-A2/BGA1226

MCHG

N5	VSS_91	REV:1.3	VSS_271	B10
M37	VSS_92		VSS_272	A39
M35	VSS_93		VSS_273	A34
M33	VSS_94		VSS_274	A26
M27	VSS_95		VSS_275	A18
M21	VSS_96		VSS_276	A12
M20	VSS_97		VSS_277	A7
M17	VSS_98		VSS_278	BC41
M15	VSS_99		VSS_279	BC3
M10	VSS_100		VSS_280	BA1
M7	VSS_101		VSS_281	AY40
M1	VSS_102		VSS_282	AF23
L40	VSS_103		VSS_283	AF21
L33	VSS_104		VSS_284	AF19
L32	VSS_105		VSS_285	AE18
L31	VSS_106		VSS_286	AC18
L29	VSS_107		VSS_287	AA18
L21	VSS_108		VSS_288	V29
L20	VSS_109		VSS_289	U29
L11	VSS_110		VSS_290	U27
L7	VSS_111		VSS_291	R21
L5	VSS_112		VSS_292	E1
L3	VSS_113		VSS_293	C43
K43	VSS_114		VSS_294	C1
K26	VSS_115		VSS_295	A41
K21	VSS_116		VSS_296	A5
K18	VSS_117		VSS_297	A3
K13	VSS_118		VSS_298	V30
K12	VSS_119			
K2	VSS_120			
J38	VSS_121			
J35	VSS_122			
J32	VSS_123			
J27	VSS_124			
J21	VSS_125			
J9	VSS_126			
J7	VSS_127			
J5	VSS_128			
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F35	VSS_147			
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F21	VSS_149			
F18	VSS_150			
F15	VSS_151			
F3	VSS_152			
E43	VSS_153			
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E11	VSS_157			
E9	VSS_158			
E3	VSS_159			
D40	VSS_160			
D31	VSS_161			
D21	VSS_162			
D17	VSS_163			
D15	VSS_164			
D3	VSS_165			
C26	VSS_166			
C11	VSS_167			
C6	VSS_168			
C5	VSS_169			
C4	VSS_170			
B37	VSS_171			
B32	VSS_172			
B31	VSS_173			
B26	VSS_174			
B23	VSS_175			
B22	VSS_176			
B19	VSS_177			
B14	VSS_178			
D16	VSS_179			
M11	VSS_180?????			

LE82BLP-A2/BGA1226

Gigabyte Technology

Title

GMCH-GND

Size
Custom

Document Number

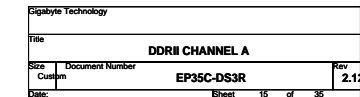
EP35C-DS3R

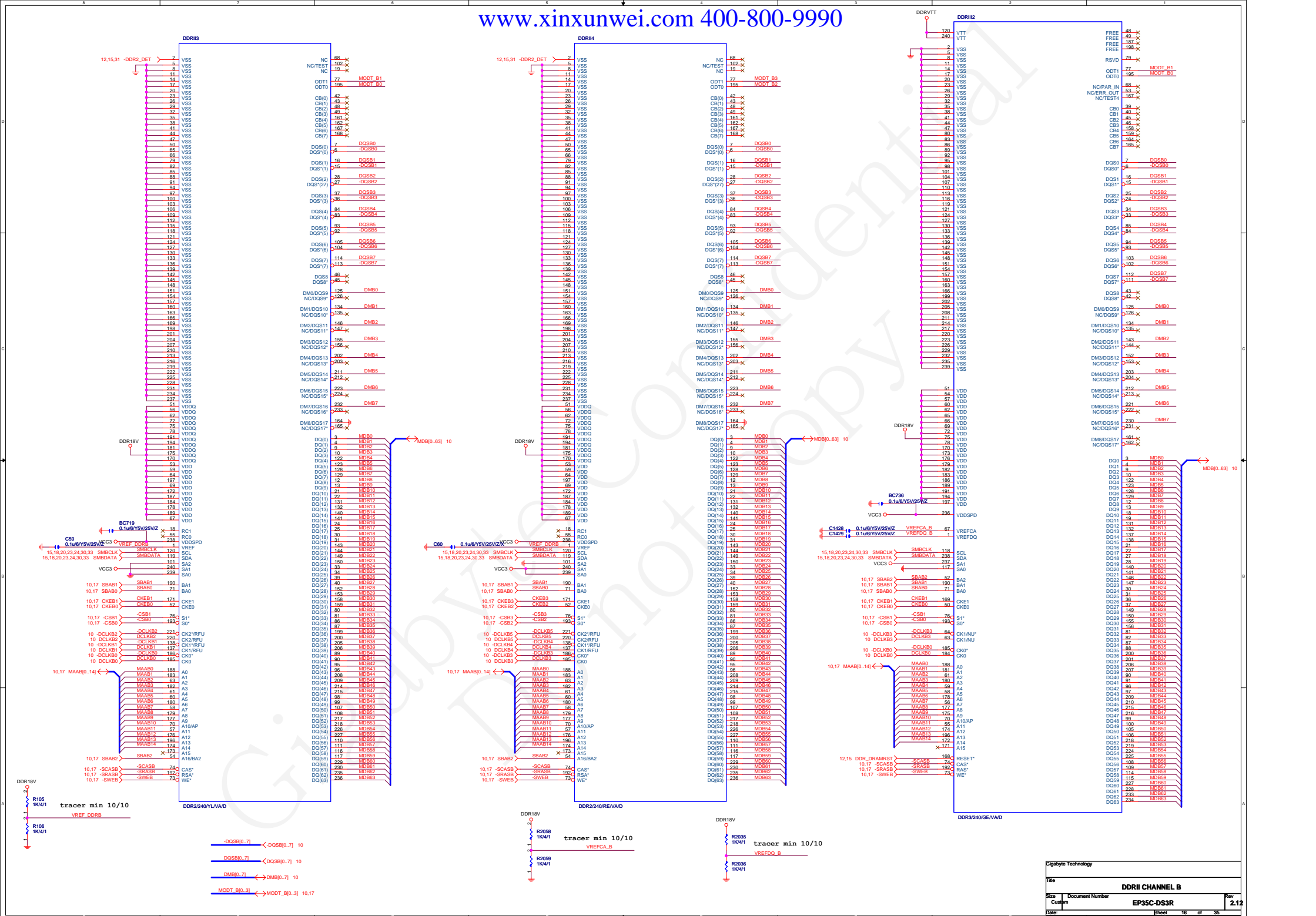
Rev

2.12

Date: Thursday, December 06, 2007

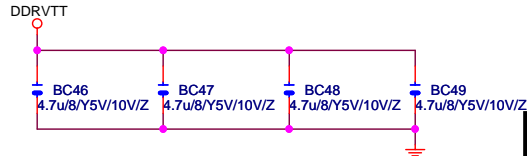
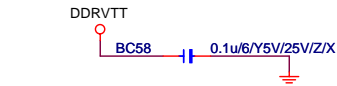
Sheet 13 of 35



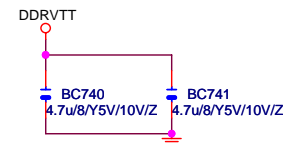
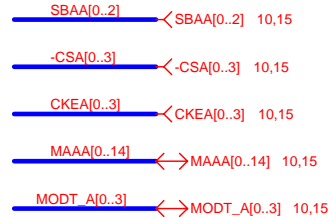
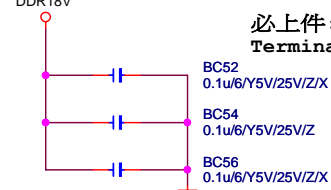


DDR TERMINATION CHANNEL A

DDRVTT Decouple



DDR18V Decouple



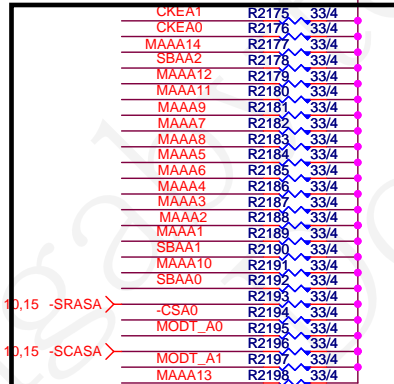
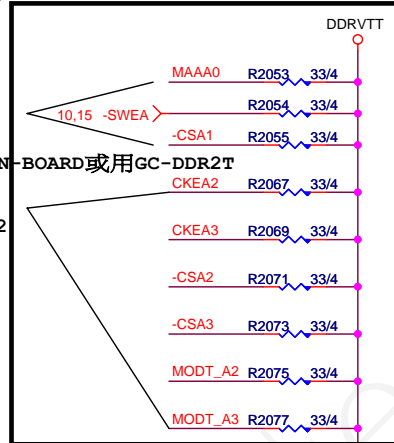
DDRVTT Decouple



FOR
DDR11, 2

必上件:
Terminator不管是用ON-BOARD或用GC-DDR2T

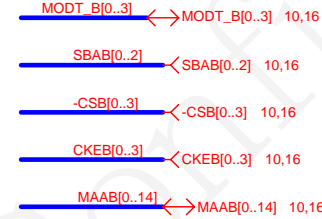
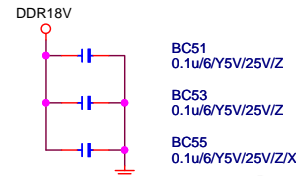
FOR
DDR11, 2



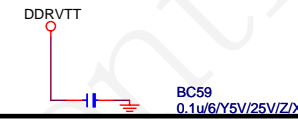
插GC-DDR2T時,不上件

DDR TERMINATION CHANNEL B

DDR18V Decouple

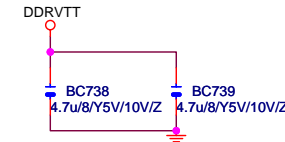
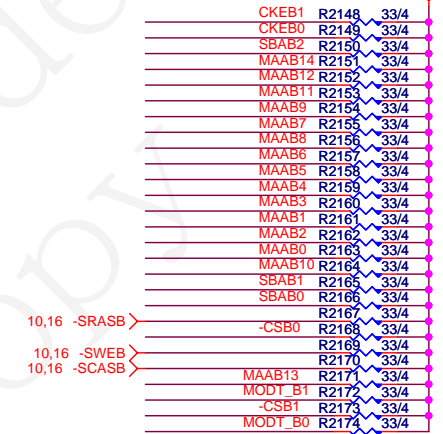


DDRVTT Decouple

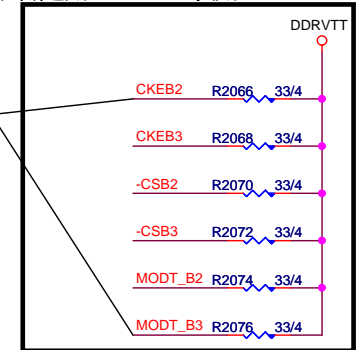


FOR
DDR11, 2

插GC-DDR2T時,不上件

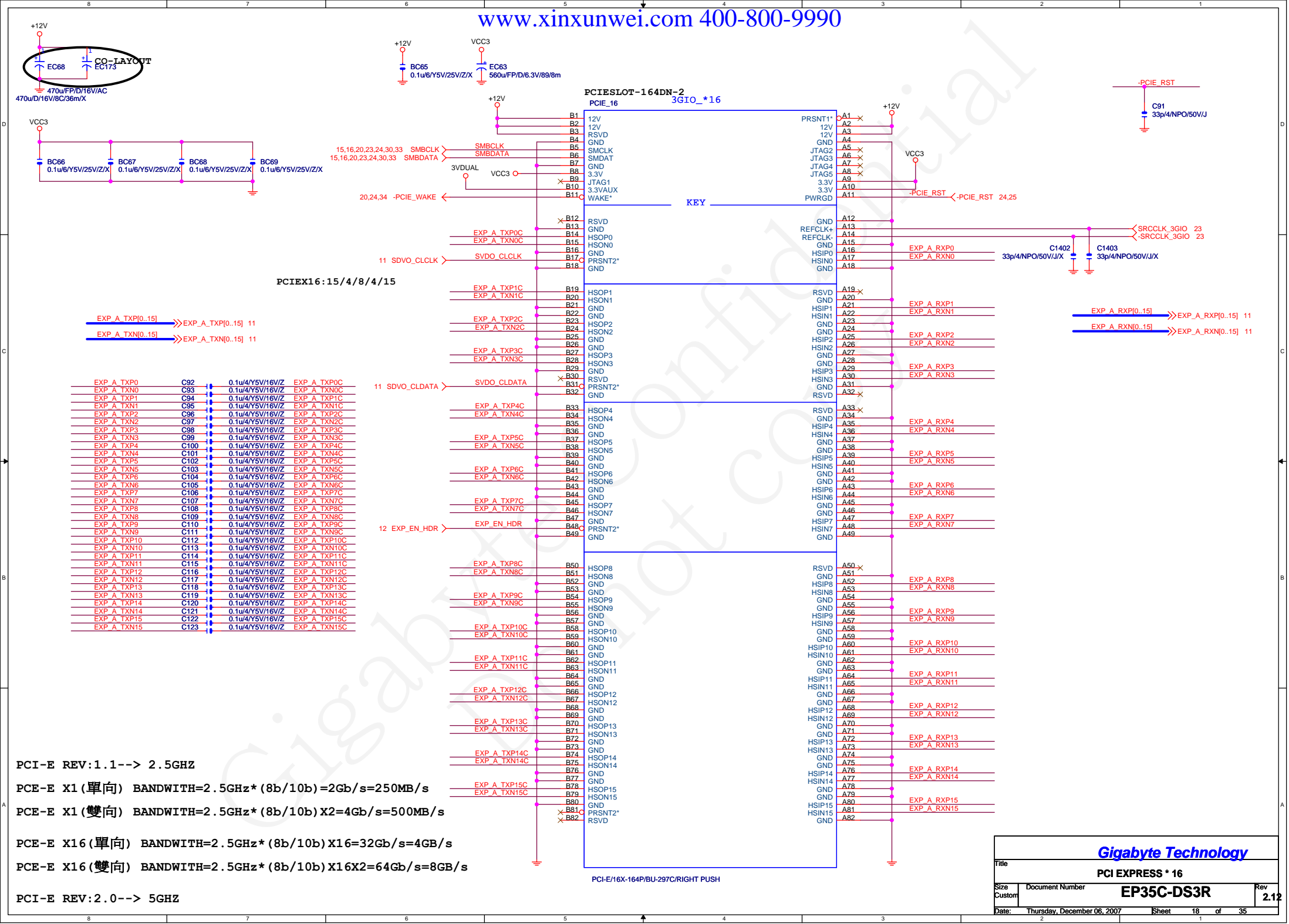


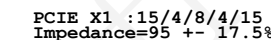
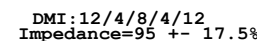
必上件:
Terminator不管是用ON-BOARD或用GC-DDR2T



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DDR11 TERMINATOR

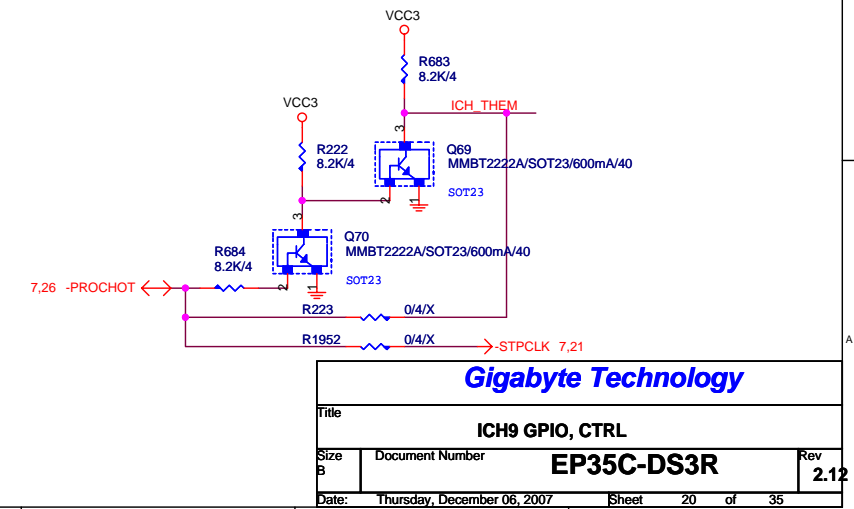
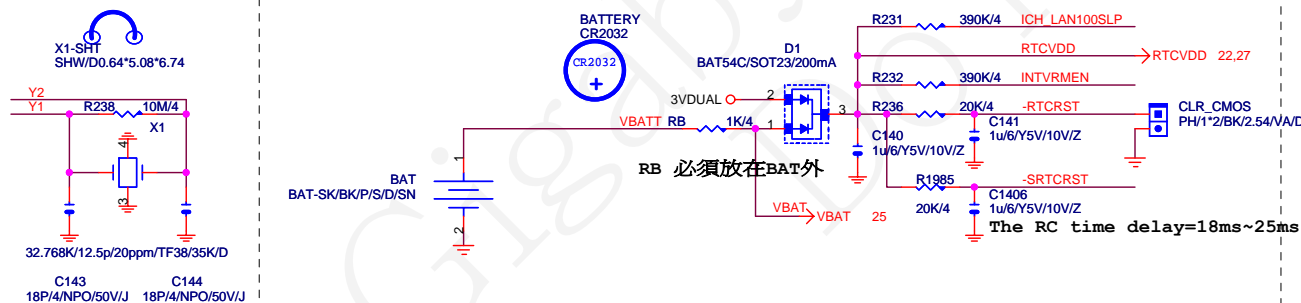
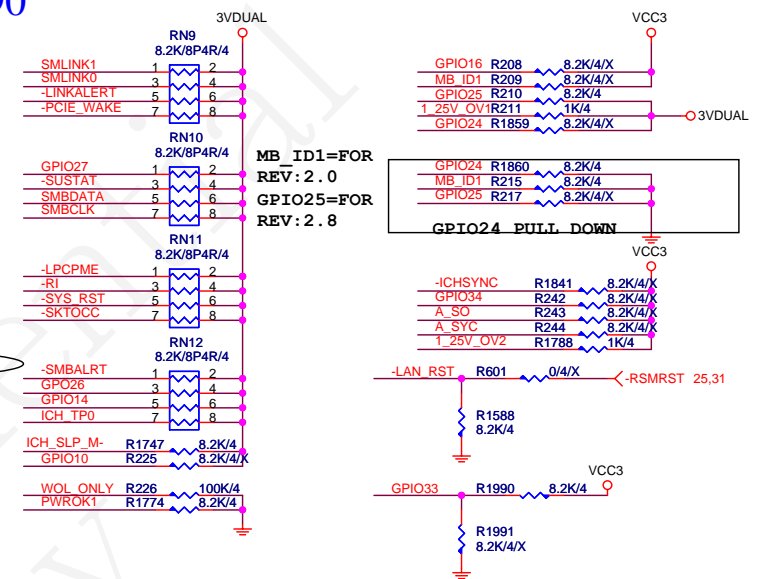
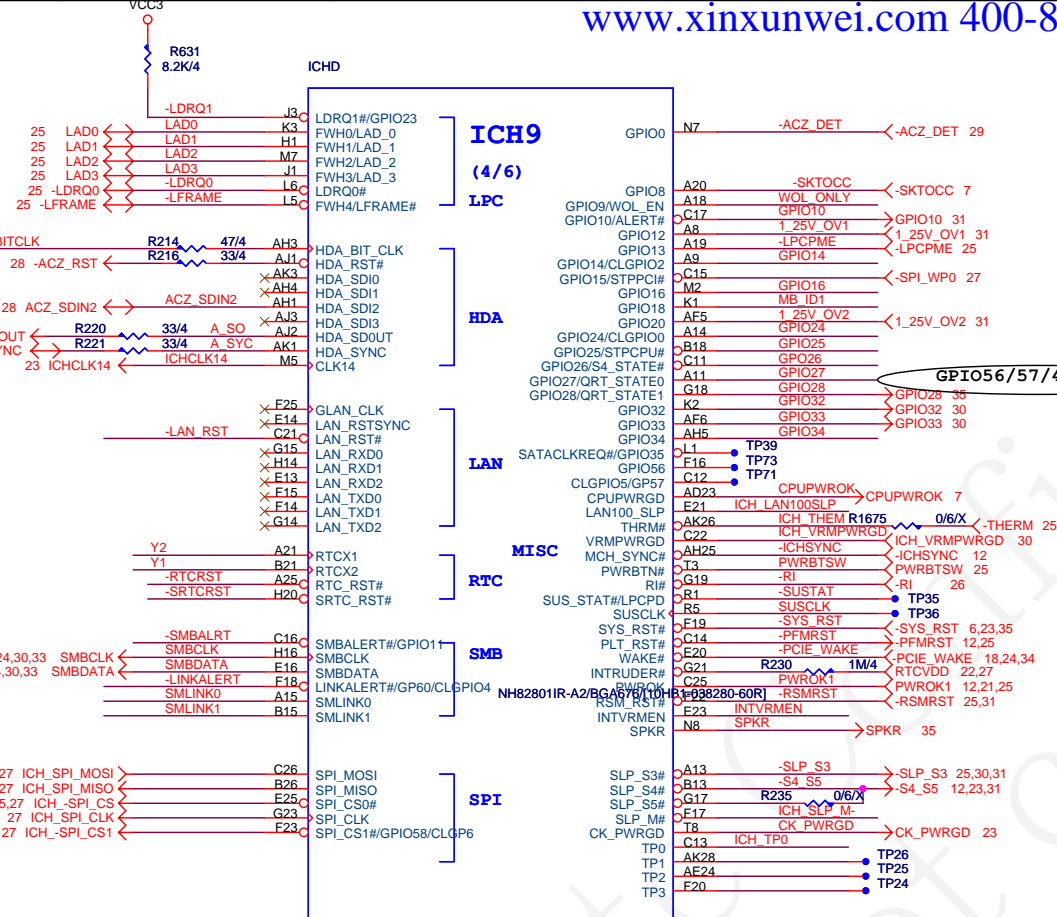


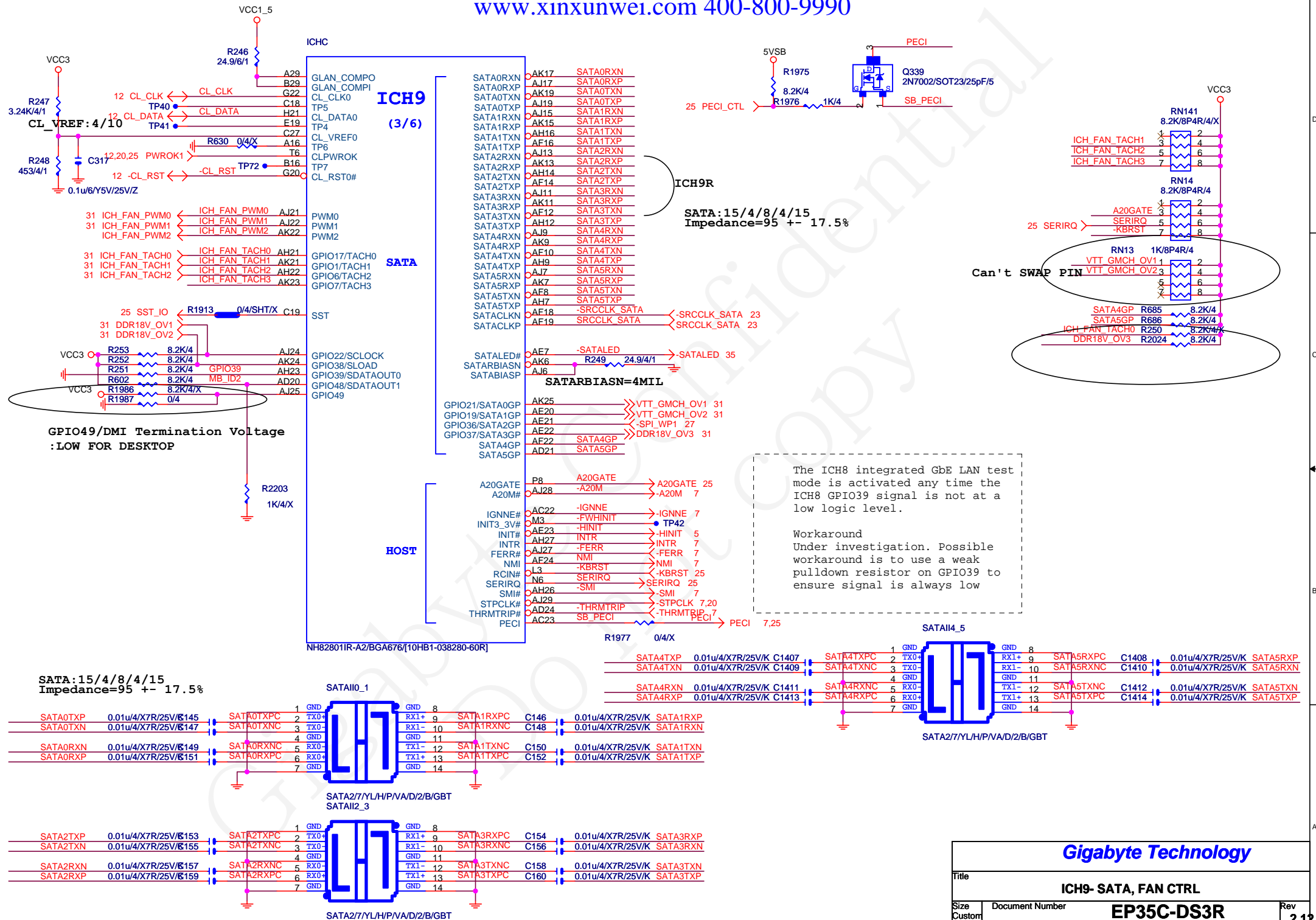


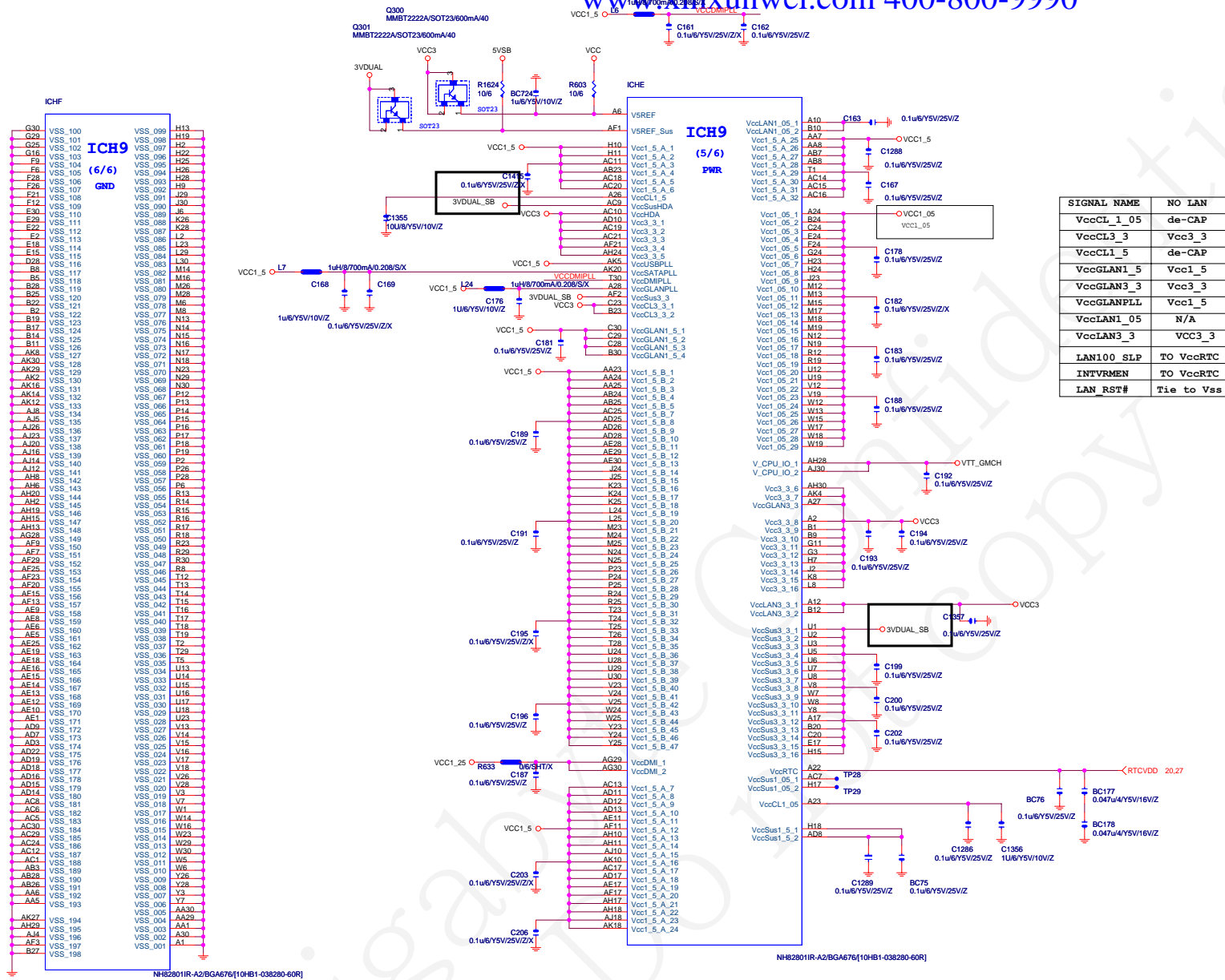
Title	ICH9-PCI, DMI, LAN, USB
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Size B	Document Number EP35C-DS3R	Rev 2.12
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Date: Thursday, December 06, 2007 Sheet 19 of 35







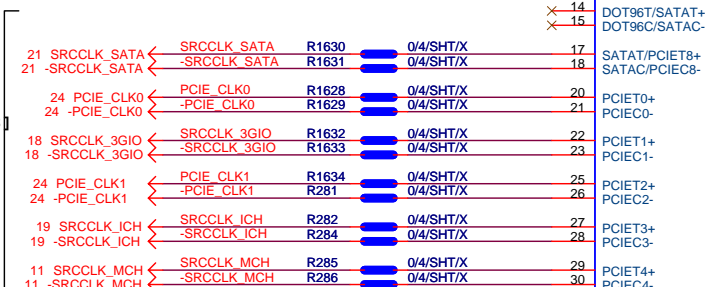
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VccCL3_3	Vcc3_3
VccCL1_5	de-CAP
VccGLAN1_5	Vcc1_5
VccGLAN3_3	Vcc3_3
VccGLANPLL	Vcc1_5
VccLAN1_05	N/A
VccLAN3_3	VCC3_3
LAN100 SLP	TO VccRTC
INTVRMEN	TO VccRTC
LAN_RST#	Tie to Vss

CLK GEN CK505

50 歐姆: [18/4/10/4/18]



50 歐姆: [18/4/10/4/18]



50 歐姆: [18/4/10/4/18]



50歐姆:[4/10]



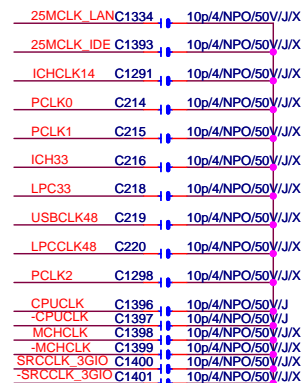
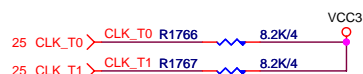
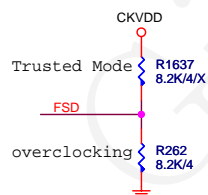
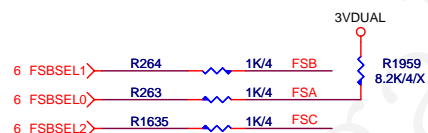
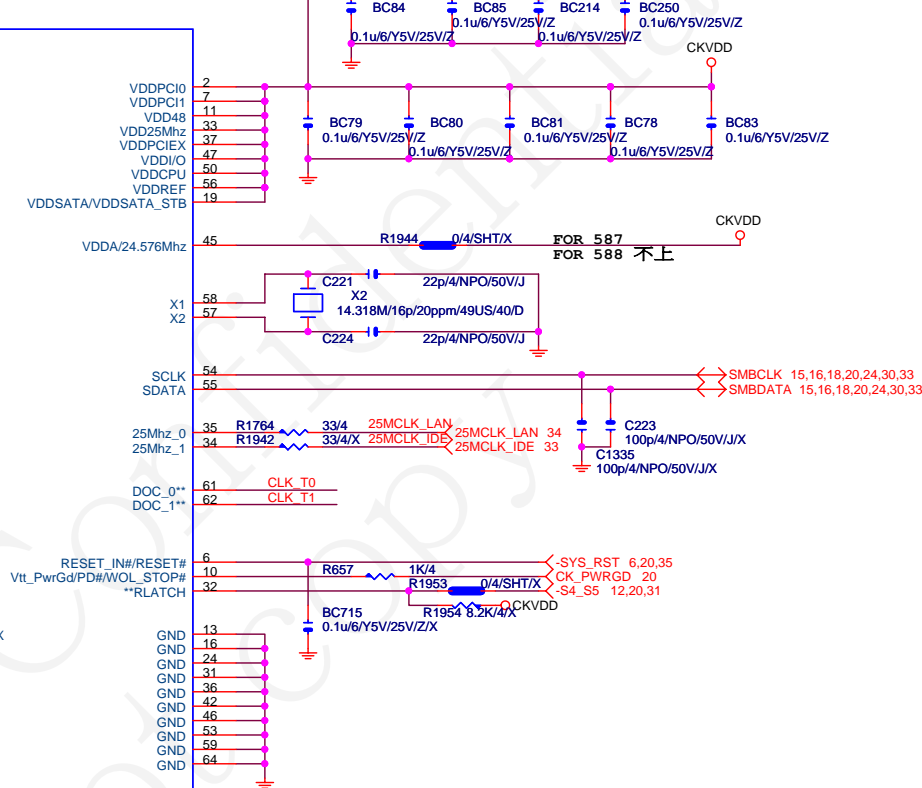
50 歐姆: [4/10]



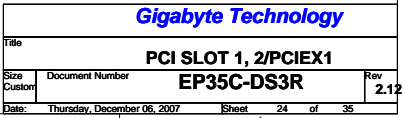
50歐姆:[4/10]



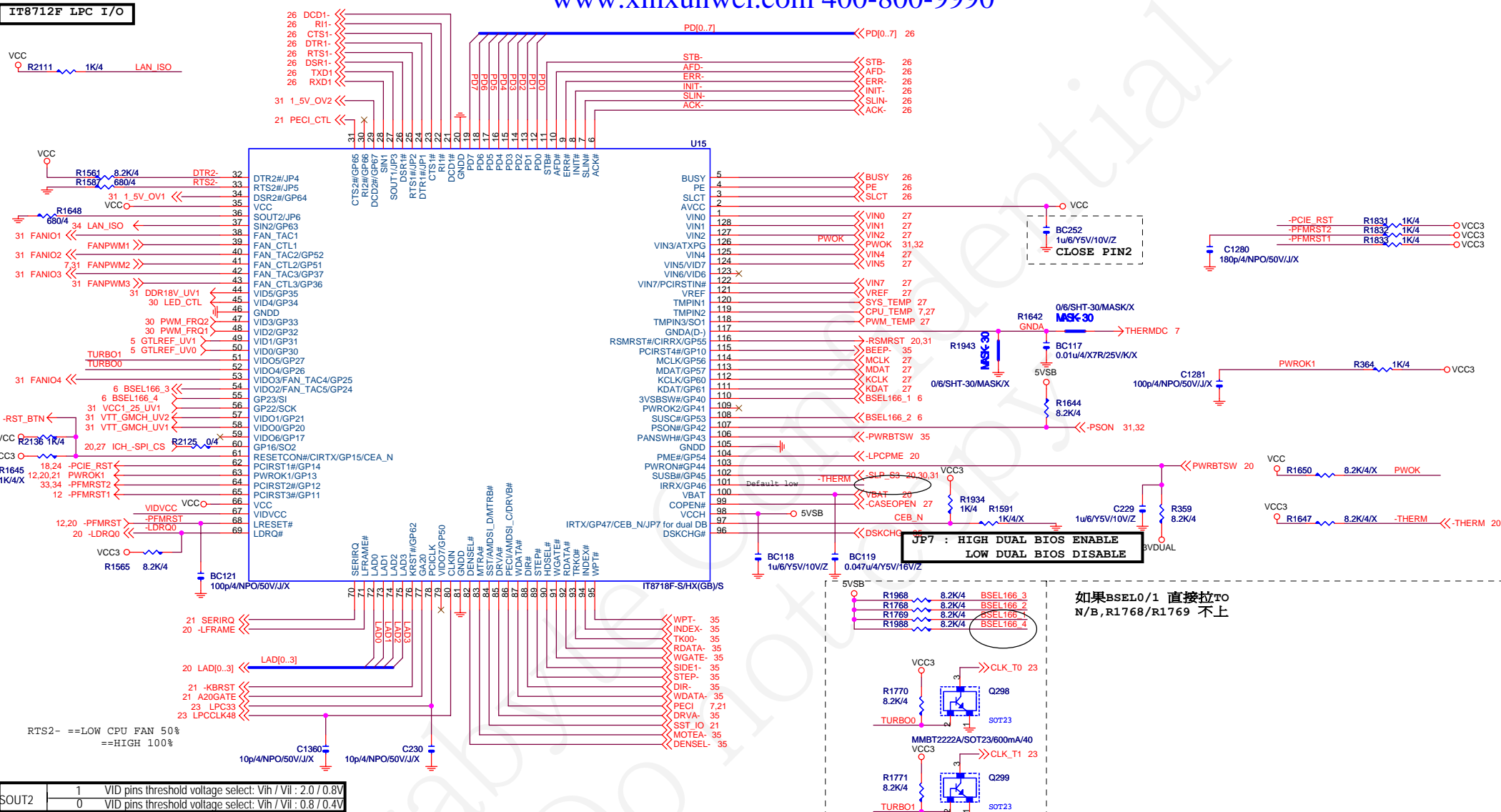
```
GSEL=1,96Mhz from 14/15,SATACLK from 17/18
GSEL=0,SATACLK from 14/15,PCIECLK from
17/18
```



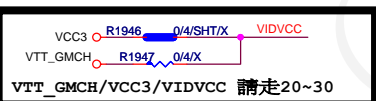
Gigabyte Technology



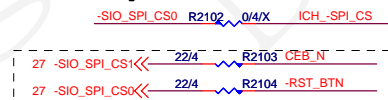
IT8712F LPC I/O



```
1.2V or 3.3V tolerance select.
1.2V OUTPUT 接 VTT_GMCH
3.3V OUTPUT 接3.3V
LPCPD#=VIDVCC
```

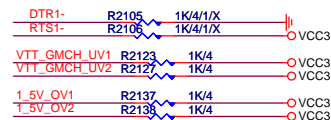


Pop to disable Dual BIOS

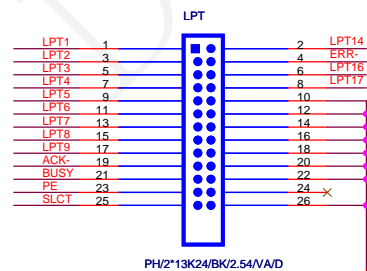
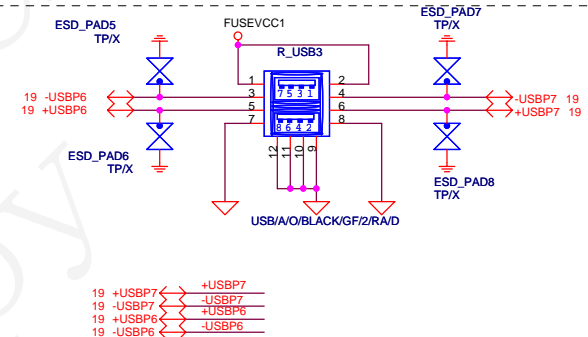
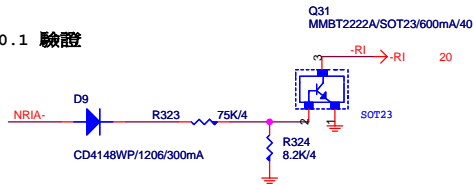


DTR1-:Low SPI flash enable

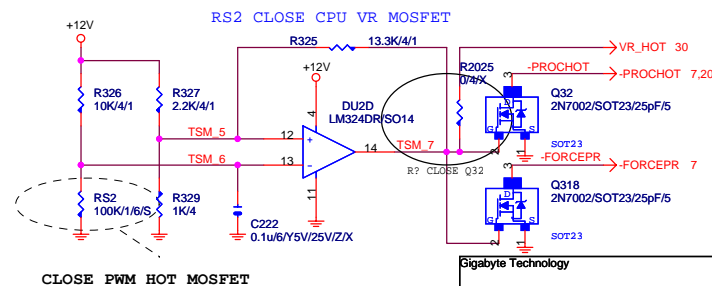
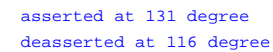
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RTS1-:Low SPI flash FOR S02
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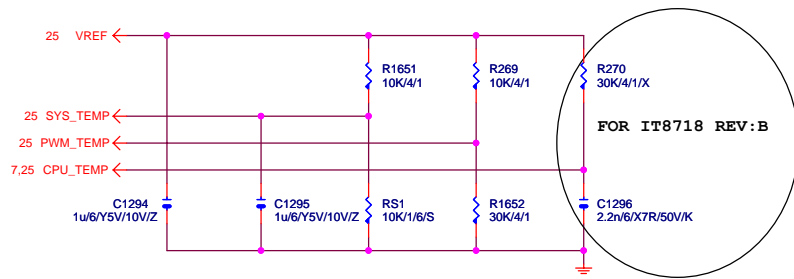
如果BSEL0/1 直接拉TO
N/B,R1768/R1769 不上



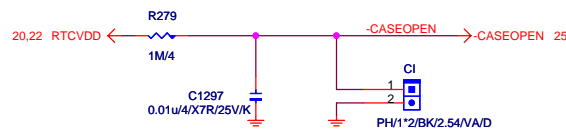
-PROHOT



TEMP H/W MONITOR

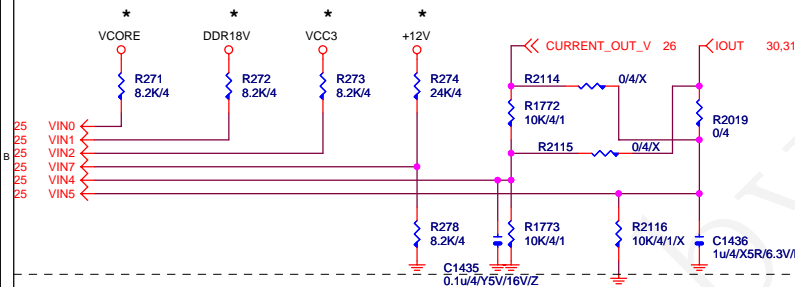


CASE OPEN

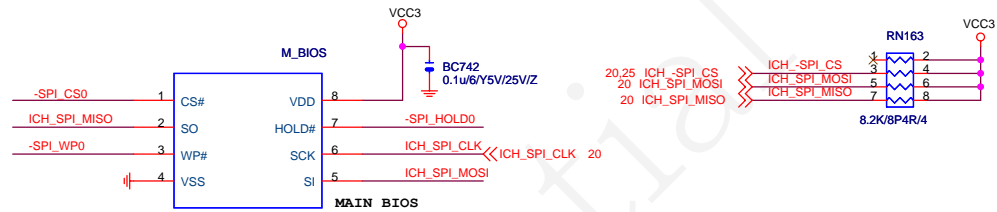
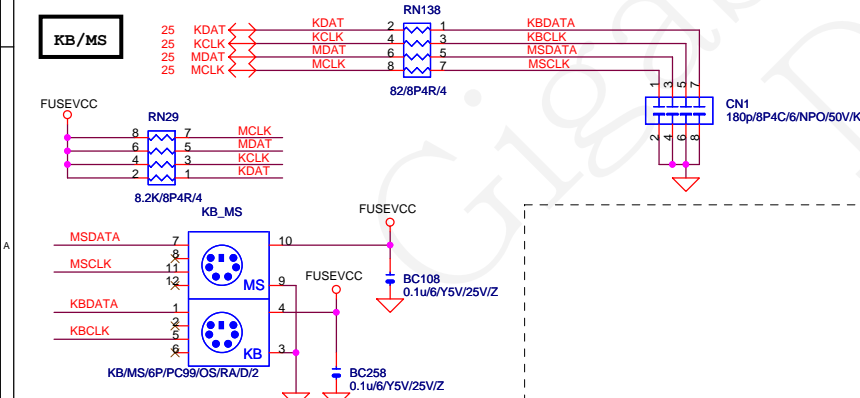


Case Open Circuits

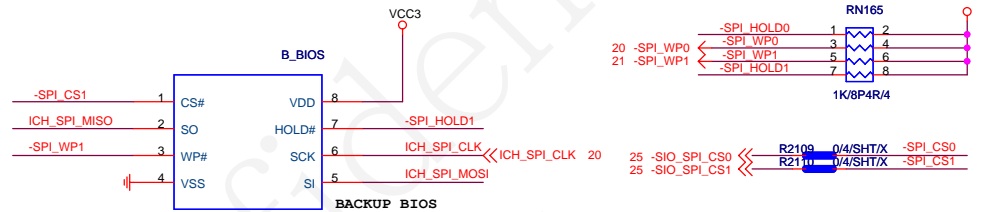
VOLTAGE-- H/W MONITOR



KB/MS

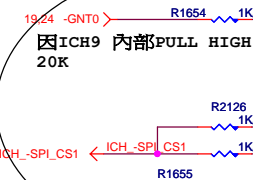


SST25VF080B-50-4C-S2AF/S[10HP4-112580-11R_10HP4-172580-01R_10HP4-152580-11R]
MXIC (4K) /SST (4K)



SST25VF080B-50-4C-S2AF/S[10HP4-112580-11R_10HP4-172580-01R_10HP4-152580-11R]
MXIC (4K) /SST (4K)

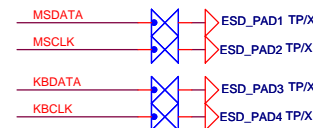
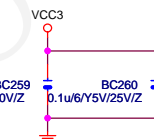
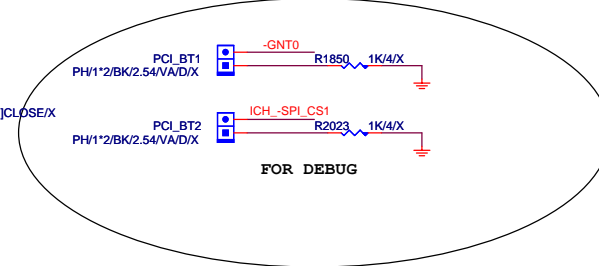
BOOT DEVICE	GNT0	CS1
SPI	0	1
PCI	1	0
FWH	1	1



PCI_BT1



JP1*2BU/OH/O-:[1-2]CLOSE/X

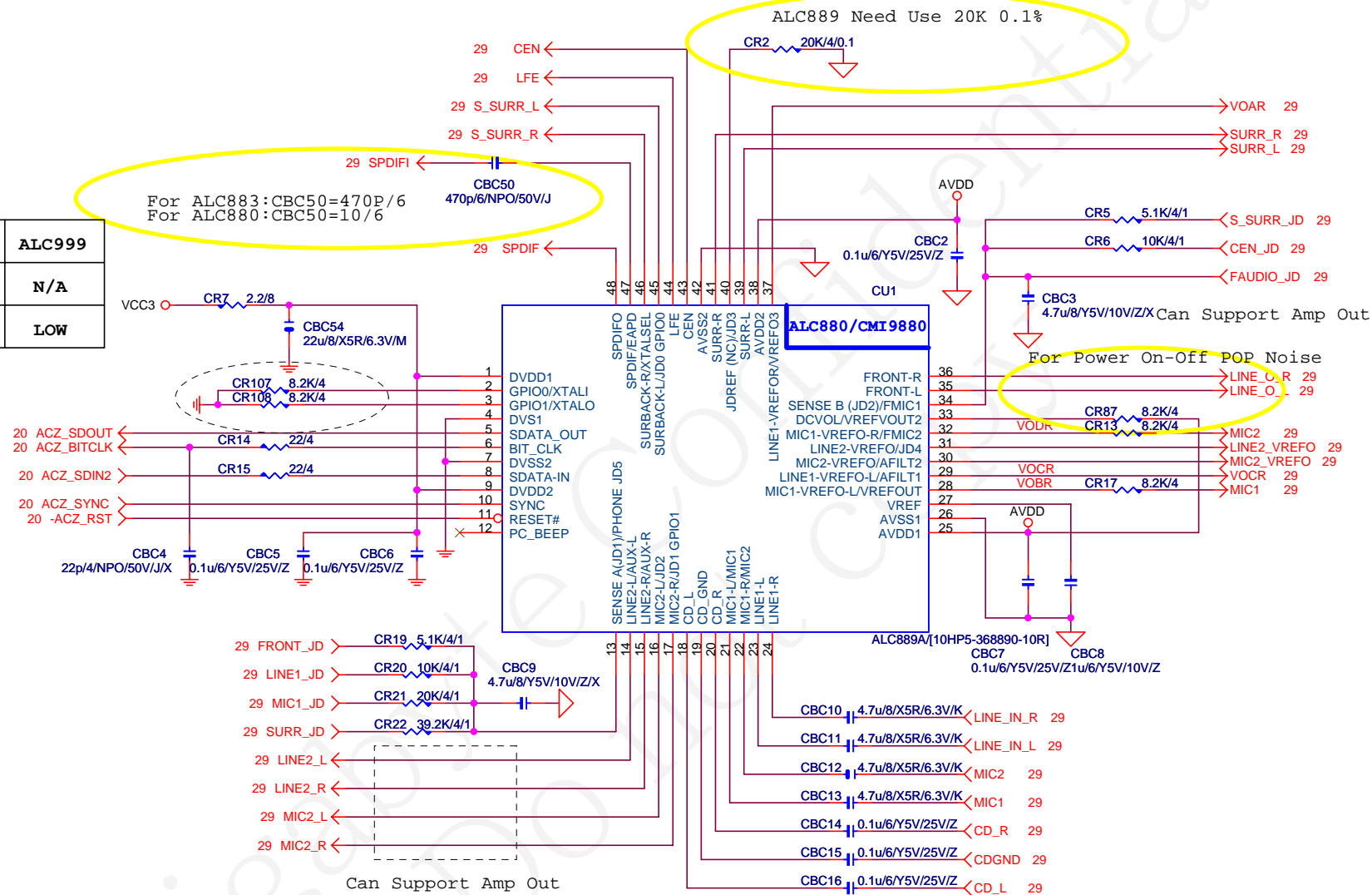


Gigabyte Technology

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BIOS/HW-MONITOR/CI/KB/MS		
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AZALIA CODEC

	ALC889A	ALC999
GPIO0	LOW	N/A
GPIO1	LOW	LOW



20 ACZ_SDOUT

20 ACZ_SYNC

20 -ACZ_RST

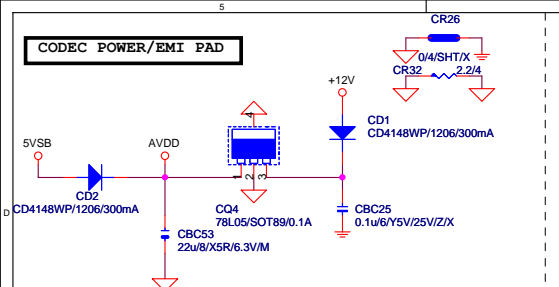
20 ACZ_BITCLK

20 ACZ_SDIN2

Gigabyte Technology

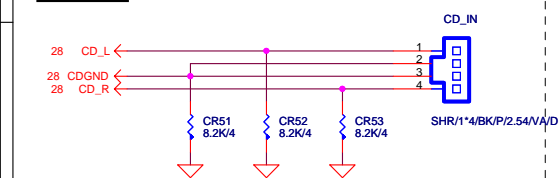
Title			ALC889A
Size	Document Number	EP35C-DS3R	
Custom		Rev	2.12
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CODEC POWER/EMI PAD



CO-LAYOUT

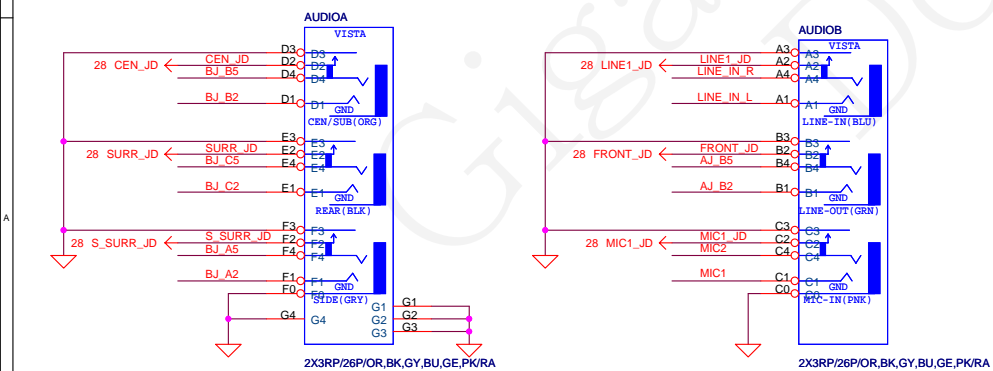
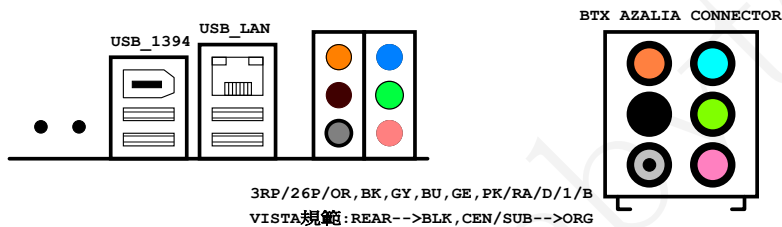
CD IN



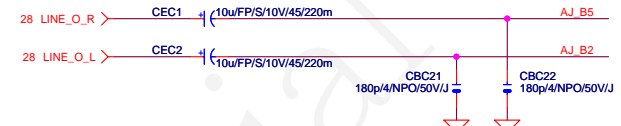
SPDIF



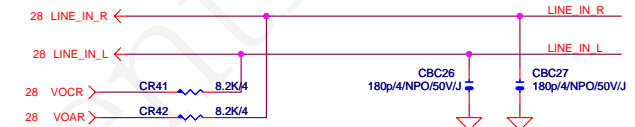
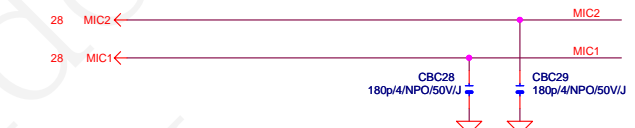
AZALIA JACK



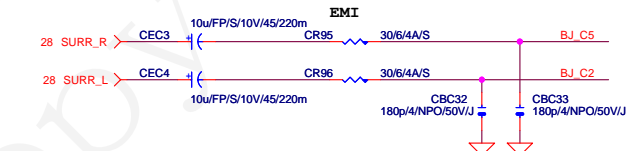
LINE-OUT



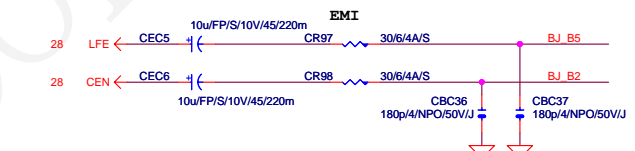
LINE-IN

**MIC-IN**

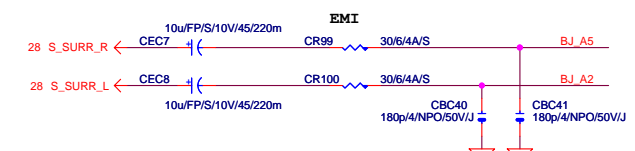
SURROUND



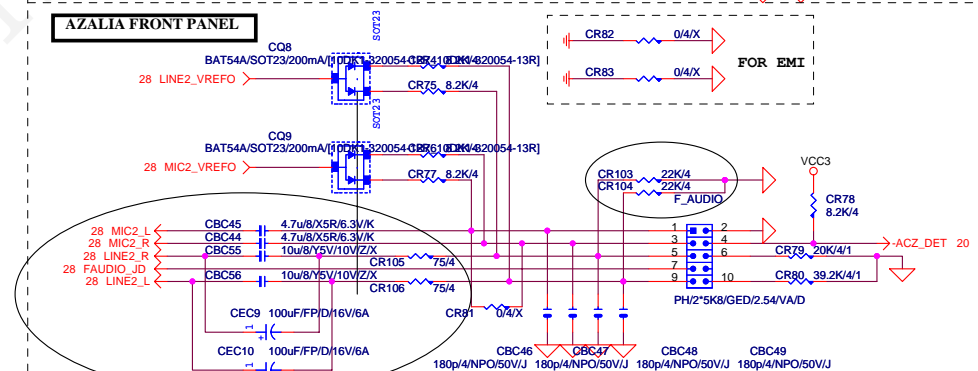
CEN/LFE



SURR BACK



AZALIA FRONT PANEL



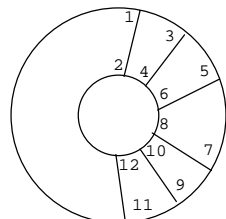
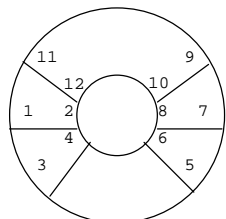
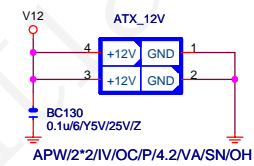
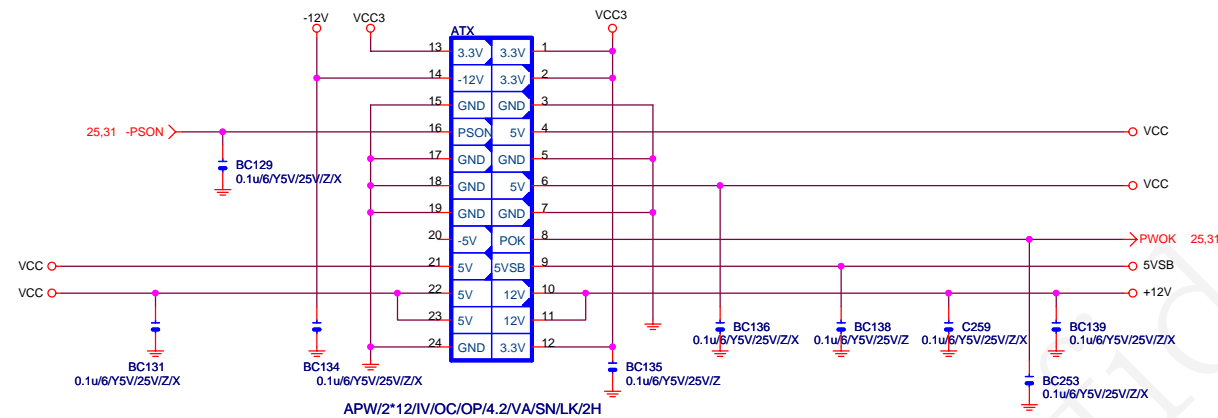
Gigabyte Technology

Title			
AUDIO JACK			
Size Custom	Document Number	EP35C-DS3R	Rev 2.12
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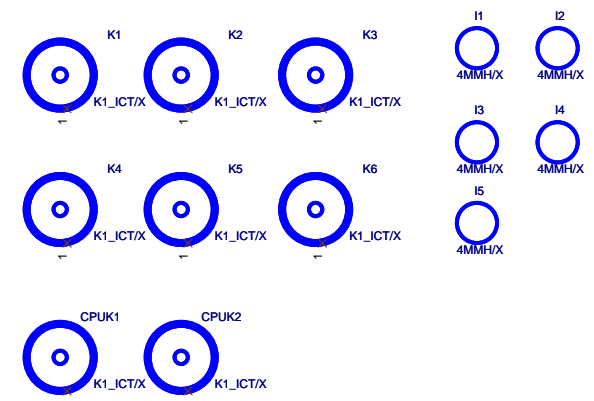
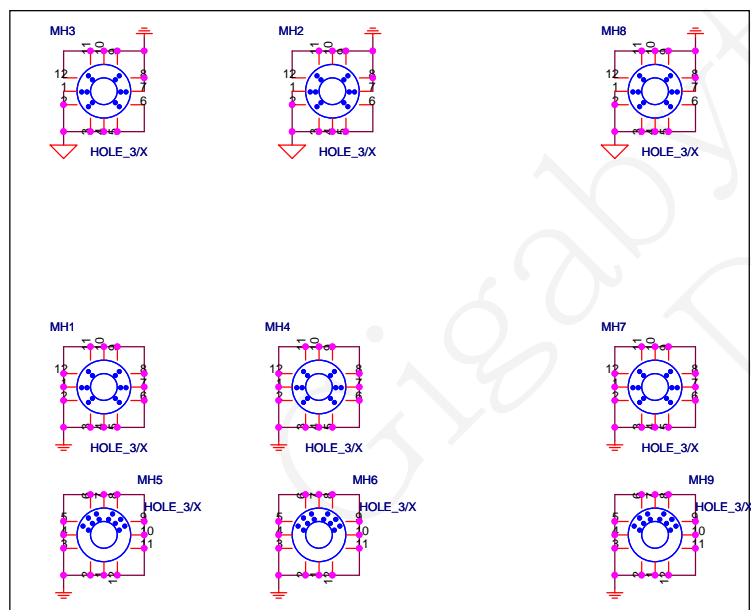




ATX POWER CONNECTOR



螺絲孔位置圖 (注意Footprint不同)

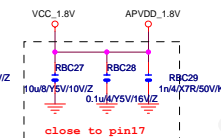
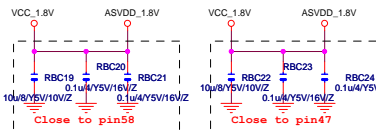
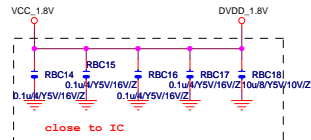
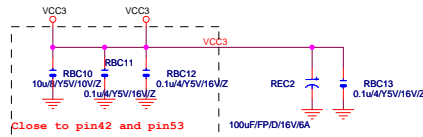
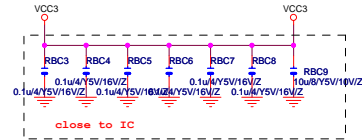
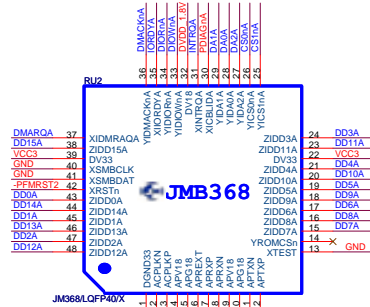
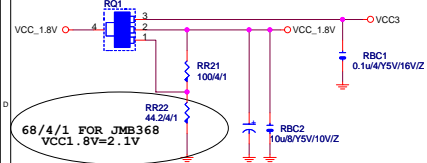


Gigabyte Technology

Title		
ATX POWER CONNECTOR		
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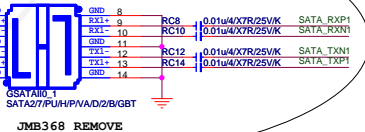
3.3V to 1.8V Voltage Regulator

APL1117/SOT223/0.8A



SATA:15/4/8/4/15 SATA Connector

Impedance=95 +/- 17.5%

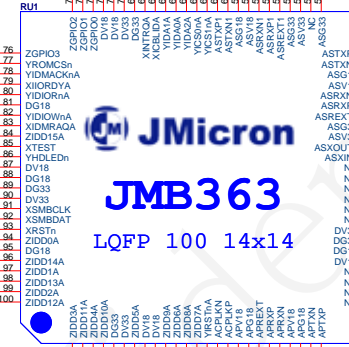
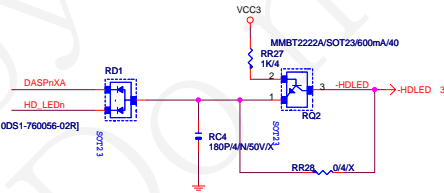
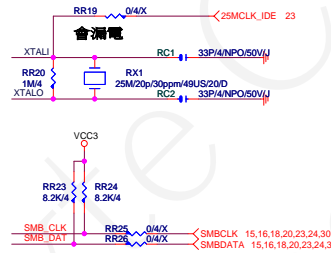
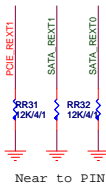


JMB368 REMOVE

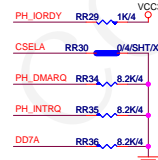
25.34 -PFMRST2

RC3

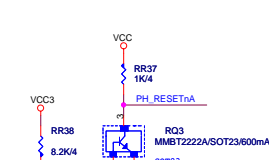
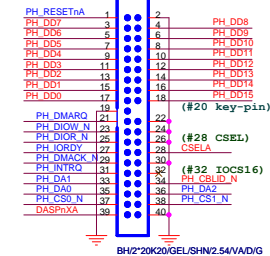
100pH/4N/50V/X



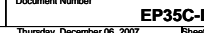
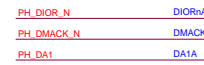
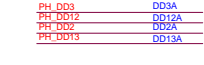
JMicron
JMB363
LQFP 100 14x14



IDE Connector



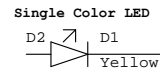
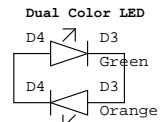
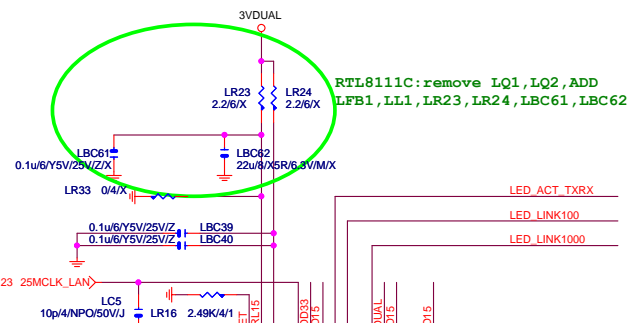
IDE Connector



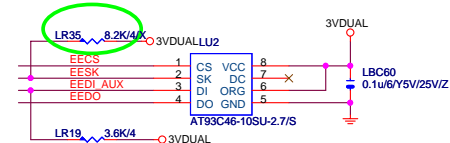
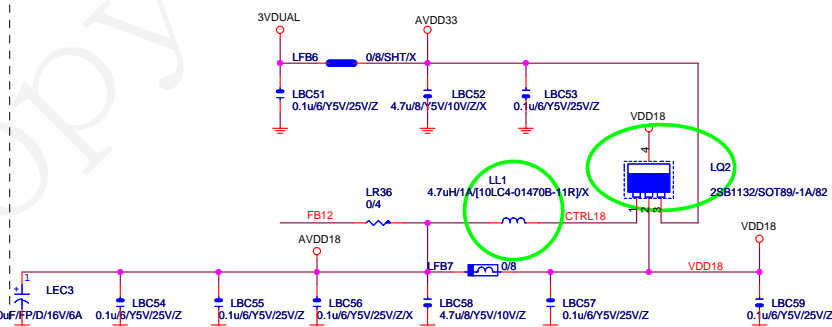
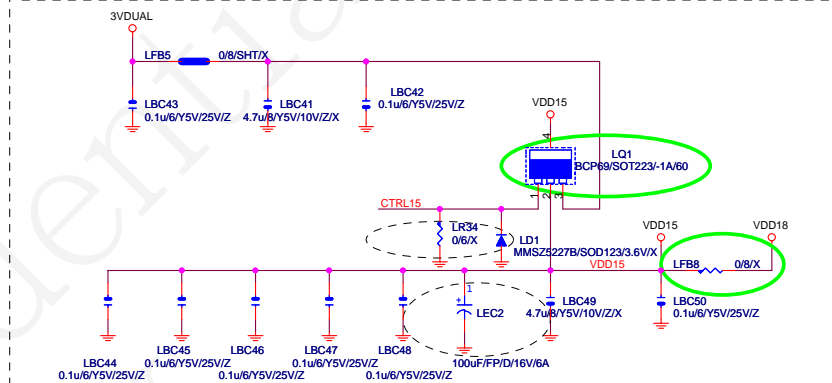
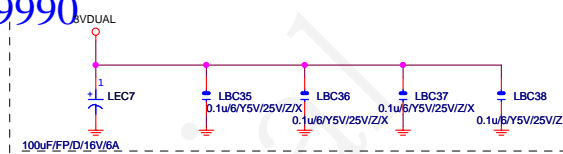
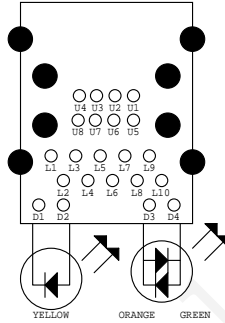
Gigabyte Technology

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PCIE-1G LAN

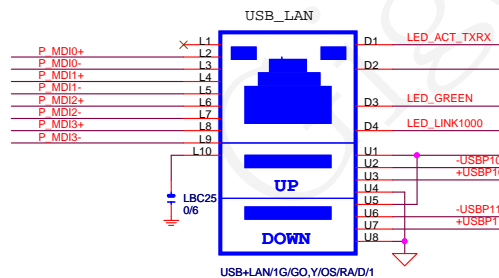


P35-152-19W9

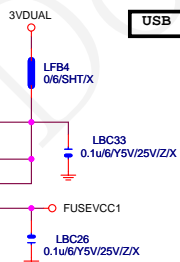


USB_LAN CONNECTOR

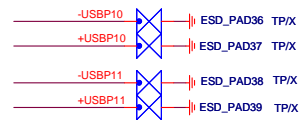
LAN 100歐姆:[30/4/8/4/30] FOR B 製程



USB POWER



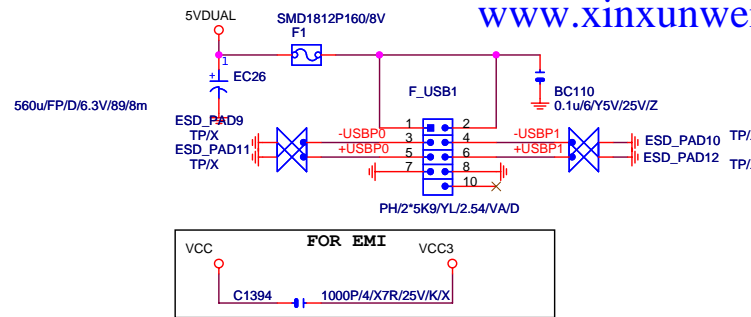
USB_LAN



Gigabyte Technology

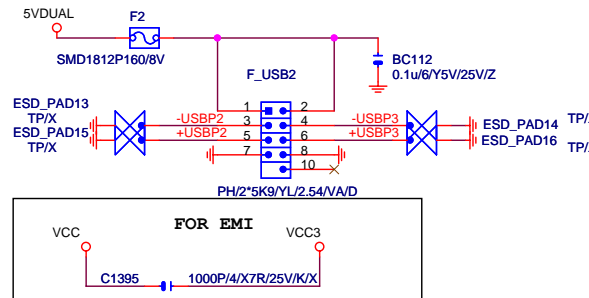
FRONT USB1

19 +USBP0 <-> +USBP0
19 -USBP0 <-> -USBP0
19 +USBP1 <-> +USBP1
19 -USBP1 <-> -USBP1



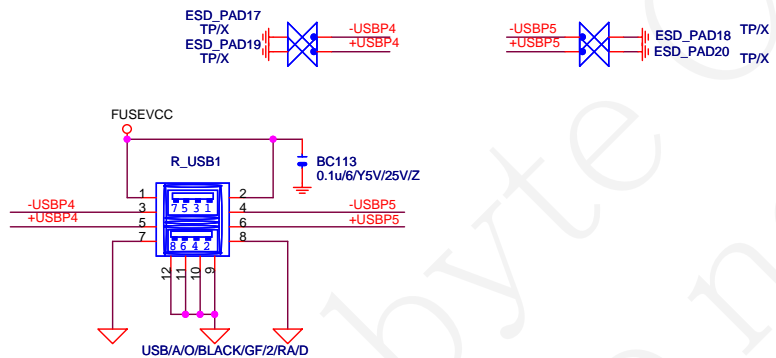
FRONT USB2

19 +USBP2 <-> +USBP2
19 -USBP2 <-> -USBP2
19 +USBP3 <-> +USBP3
19 -USBP3 <-> -USBP3

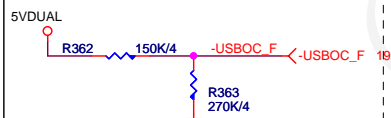


R_USB1

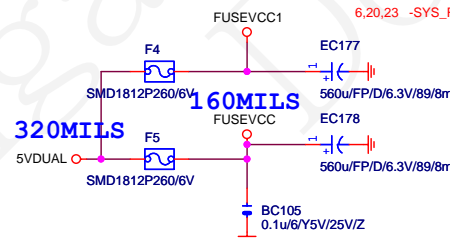
19 +USBP4 <-> +USBP4
19 -USBP4 <-> -USBP4
19 +USBP5 <-> +USBP5
19 -USBP5 <-> -USBP5



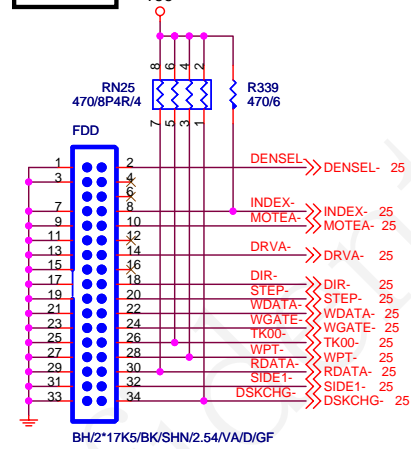
FRONT USB OC1



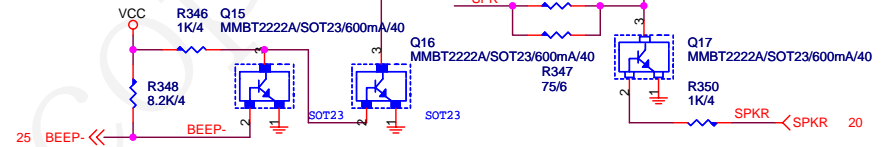
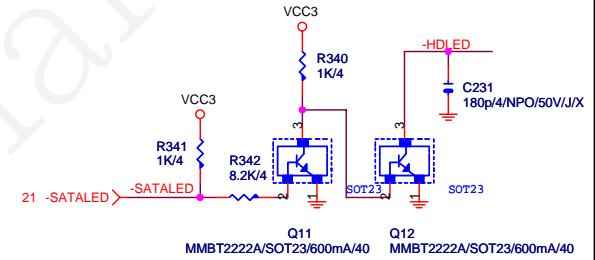
USB POWER



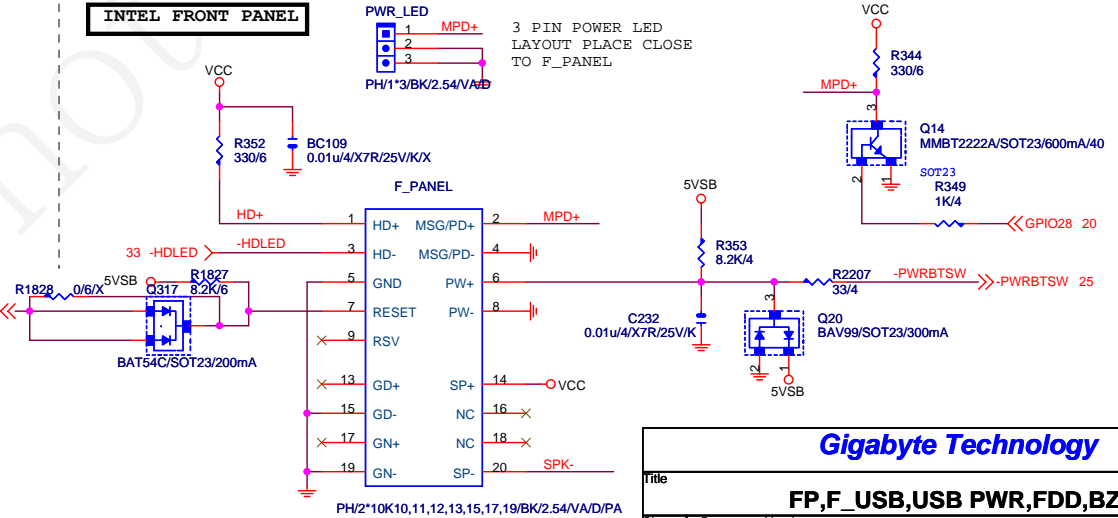
FLOPPY



SATA LED



INTEL FRONT PANEL



Gigabyte Technology

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